

Model checking memoryful linear-time logics over one-counter automata[☆]

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Abstract

We study complexity of the model-checking problems for LTL with registers (also known as freeze LTL and written LTL^\downarrow) and for first-order logic with data equality tests (written $FO(\sim, <, +1)$) over one-counter automata. We consider several classes of one-counter automata (mainly deterministic vs. nondeterministic) and several logical fragments (restriction on the number of registers or variables and on the use of propositional variables for control states). The logics have the ability to store a counter value and to test it later against the current counter value. We show that model checking LTL^\downarrow and $FO(\sim, <, +1)$ over deterministic one-counter automata is PSPACE-complete with infinite and finite accepting runs. By contrast, we prove that model checking LTL^\downarrow in which the until operator U is restricted to the eventually F over nondeterministic one-counter automata is Σ_1^1 -complete [resp. Σ_1^0 -complete] in the infinitary [resp. finitary] case even if only one register is used and with no propositional variable. As a corollary of our proof, this also holds for $FO(\sim, <, +1)$ restricted to two variables (written $FO_2(\sim, <, +1)$). This makes a difference with the facts that several verification problems for one-counter automata are known to be decidable with relatively low complexity, and that finitary satisfiability for LTL^\downarrow and $FO_2(\sim, <, +1)$ are decidable. Our results pave the way for model-checking memoryful (linear-time) logics over other classes of operational models, such as reversal-bounded counter machines.

Keywords: one-counter automaton, temporal logic, first-order logic, computational complexity

1. Introduction

Logics for data words. Data words are sequences in which each position is labelled by a letter from a finite alphabet and by another letter from an infinite alphabet (the datum). This fundamental and simple model arises in systems that are potentially unbounded in some way. Typical examples are runs of counter systems [1], timed words accepted by timed automata [2] and runs of systems with unboundedly many parallel components (data are component indices) [3]. The extension to trees makes also sense to model XML documents with values, see e.g. [4, 5, 6]. In order to really speak about data, known logical formalisms for data words/trees contain a mechanism that stores a value and tests it later against other values, see e.g. [7, 8]. This is a powerful feature shared by other memoryful temporal logics [9, 10]. However, the satisfiability problem for these logics becomes easily undecidable even when stored data can be tested only for equality. For instance, first-order logic for data words restricted to three individual variables is undecidable [7] and LTL with registers (also known as freeze LTL) restricted to a single register is undecidable over infinite data words [8]. By contrast, decidable fragments of the satisfiability problems have been found in [11, 7, 12, 8, 13] either by imposing syntactic restrictions (bound the number of registers, constrain the polarity of temporal formulae, etc.) or by considering subclasses of data words (finiteness for example). Similar phenomena occur with metric temporal logics and timed words [14, 15]. A key point for all these logical formalisms is the ability to store a value from an infinite alphabet, which is a feature also present in models of register

[☆]Supported by the Agence Nationale de la Recherche, grant ANR-06-SETIN-001.

automata, see e.g. [16, 17, 18, 19]. However, the storing mechanism has a long tradition (apart from its ubiquity in programming languages) since it appeared for instance in real-time logics [20] (the data are time values) and in so-called hybrid logics (the data are node addresses), see an early undecidability result with reference pointers in [21]. Meaningful restrictions for hybrid logics can also lead to decidable fragments, see e.g. [22].

Our motivations. In this paper, our main motivation is to analyze the effects of adding a binding mechanism with registers to specify runs of operational models such as pushdown systems and counter automata. The registers are simple means to compare data values at different points of the execution. Indeed, runs can be naturally viewed as data words: for example, the finite alphabet is the set of control states and the infinite alphabet is the set of data values (natural numbers, stacks, etc.). To do so, we enrich an ubiquitous logical formalism for model-checking techniques, namely linear-time temporal logic LTL, with registers. Even though this was the initial motivation to introduce LTL with registers in [12], most decision problems considered in [12, 13, 8] are essentially oriented towards satisfiability. In this paper, we focus on the following type of model-checking problem: given a set of runs generated by an operational model, more precisely by a one-counter automaton, and a formula from LTL with registers, is there a run satisfying the given formula? In our context, it will become clear that the extension with two counters is undecidable. It is not difficult to show that this model-checking problem differs from those considered in [13, 12] and from those in [23, 24, 25] dealing with so-called hybrid logics. However, since two consecutive counter values in a run are ruled by the set of transitions, constraints on data that are helpful to get fine-tuned undecidability proofs for satisfiability problems in [12, 8] may not be allowed on runs. This is precisely what we want to understand in this work. As a second main motivation, we would like to compare the results on LTL with registers with those for first-order logic with data equality tests. Indeed, LTL (with past-time operators) and first-order logic are equivalently expressive by Kamp’s theorem, but such a correspondence in presence of data values is not known. Our investigation about the complexity of model-checking one-counter automata with memoryful logics include then first-order logic.

Our contribution. We study complexity issues related to the model-checking problem for LTL with registers over one-counter automata that are simple operational models, but our undecidability results can be obviously lifted to pushdown systems when registers store the stack value. Moreover, in order to determine borderlines for decidability, we also present results for deterministic one-counter models that are less powerful but remain interesting when they are viewed as a mean to specify an infinite path on which model checking is performed, see analogous issues in [26].

We consider several classes of one-counter automata (deterministic, weakly deterministic and nondeterministic) and several fragments by restricting the use of registers or the use of letters from the finite alphabet. Moreover, we distinguish finite accepting runs from infinite ones as data words. Unlike results from [14, 15, 8, 13], the decidability status of the model checking does not depend on the fact that we consider finite data words instead of infinite ones. In this paper, we establish the following results.

- Model checking LTL with registers [resp. first-order logic with data equality test] over deterministic one-counter automata is PSPACE-complete (see Sect. 3.3). PSPACE-hardness is established by reducing QBF and it also holds when no letters from the finite alphabet are used in formulae. In order to get these complexity upper bounds, we translate our problems into model-checking first-order logic without data equality test over ultimately periodic words that can be solved in polynomial space thanks to [26].
- Model checking LTL with registers over nondeterministic one-counter automata restricted to a unique register and without alphabet is Σ_1^1 -complete in the infinitary case by reducing the recurrence problem for Minsky machines (see Sect. 4). In the finitary case, the problem is shown Σ_1^0 -complete by reducing the halting problem for Minsky machines. These results are quite surprising since several verification problems for one-counter automata are decidable with relatively low complexity [27, 28, 29]. Moreover, finitary satisfiability for LTL with one register is decidable [8] even though with non-primitive recursive complexity. These results can be also obtained for first-order logic with data equality test restricted to two variables by analysing the structure of formulae used in the undecidability proofs and by using [8].

Figure 1 contains a summary of the main results we obtained; notations are fully explained in Section 2. For instance, $\text{MC}(\text{LTL})_1^\omega[\mathbf{X}, \mathbf{F}]$ refers to the existential model-checking problem on infinite accepting runs from

PSPACE-completeness for det. 1CA	Σ_1^0 -completeness for 1CA	Σ_1^1 -completeness for 1CA
MC(LTL) $^\omega$, MC(LTL) * MC(LTL) $^\omega$ [F], MC(LTL) * [X, F]	MC(LTL) $_1^*$ [X, F] PureMC(LTL) $_1^*$ [X, F]	MC(LTL) $_1^\omega$ [X, F] PureMC(LTL) $_1^\omega$ [X, F]
MC(FO) $^\omega$, MC(FO) * MC(FO) $^\omega$ [\sim , <]	MC(FO) $_2^*$ [\sim , <]	MC(FO) $_2^\omega$ [\sim , <]

Figure 1: Summary of main results

one-counter automata with freeze LTL restricted to the temporal operators “next” and “sometimes”, and to a unique register. Similarly, $\text{MC}(\text{FO})_2^\omega[\sim, <]$ refers to the existential model-checking problem on finite accepting runs from one-counter automata with first-order logic on data words restricted to two individual variables.

Plan of the paper. In Sect. 2, we introduce the model-checking problem for LTL with registers over one-counter automata as well as the corresponding problem for first-order logic with data equality test. In Sect. 3, we consider decidability and complexity issues for model checking deterministic one-counter automata. In Sect. 4, several model-checking problems over nondeterministic one-counter automata are shown undecidable.

This paper is an extended version of [30] that also improves significantly the results about the PSPACE upper bounds and the undecidability results, in particular by considering first-order language over data words.

2. Preliminaries

2.1. One-counter automaton

Let us recall standard definitions and notations about our operational models. A one-counter automaton is a tuple $\mathcal{A} = \langle Q, q_I, \delta, F \rangle$ where:

- Q is a finite set of states,
- $q_I \in Q$ is the initial state,
- $F \subseteq Q$ is the set of accepting states,
- $\delta \subseteq Q \times L \times Q$ is the transition relation over the instruction set $L = \{\text{inc}, \text{dec}, \text{ifzero}\}$.

A counter valuation v is an element of \mathbb{N} and a configuration of \mathcal{A} is a pair in $Q \times \mathbb{N}$. The initial configuration is the pair $\langle q_I, 0 \rangle$. As usual, a one-counter automaton \mathcal{A} induces a (possibly infinite) transition system $\langle Q \times \mathbb{N}, \rightarrow \rangle$ such that $\langle q, n \rangle \rightarrow \langle q', n' \rangle$ iff one of the conditions below holds true:

1. $\langle q, \text{inc}, q' \rangle \in \delta$ and $n' = n + 1$,
2. $\langle q, \text{dec}, q' \rangle \in \delta$ and $n' = n - 1$ (and $n' \in \mathbb{N}$),
3. $\langle q, \text{ifzero}, q' \rangle \in \delta$ and $n = n' = 0$.

A finite [resp. infinite] *run* ρ is a finite [resp. infinite] sequence $\rho = \langle q_0, n_0 \rangle \rightarrow \langle q_1, n_1 \rangle \rightarrow \dots$ where $\langle q_0, n_0 \rangle$ is the initial configuration. A finite run $\rho = \langle q_0, n_0 \rangle \rightarrow \langle q_1, n_1 \rangle \rightarrow \dots \rightarrow \langle q_f, n_f \rangle$ is *accepting* iff q_f is an accepting state. An infinite run ρ is accepting iff it contains an accepting state infinitely often (Büchi acceptance condition). All these notations can be naturally adapted to multicounter automata.

A one-counter automaton \mathcal{A} is *deterministic* whenever it corresponds to a deterministic one-counter Minsky machine: for every state q ,

- either \mathcal{A} has a unique transition from q incrementing the counter,
- or \mathcal{A} has exactly two transitions from q , one with instruction `ifzero` and the other with instruction `dec`,

- or \mathcal{A} has no transition from q (not present in original deterministic Minsky machines [1]).

In the transition system induced by any deterministic one-counter automaton, each configuration has at most one successor. One-counter automata in full generality are understood as *non-deterministic* one-counter automata.

2.2. LTL over data words

Formulae of the logic $\text{LTL}^{\downarrow, \Sigma}$ [8] where Σ is a finite alphabet are defined as follows:

$$\phi ::= a \mid \uparrow_r \mid \neg\phi \mid \phi \wedge \phi \mid \phi \text{U} \phi \mid \mathbf{X}\phi \mid \downarrow_r \phi$$

where $a \in \Sigma$ and r ranges over $\mathbb{N} \setminus \{0\}$. We write LTL^{\downarrow} to denote LTL with registers for some unspecified finite alphabet. An occurrence of \uparrow_r within the scope of some freeze quantifier \downarrow_r is bound by it; otherwise it is free. A sentence is a formula with no free occurrence of any \uparrow_r . Given a natural number $n > 0$, we write $\text{LTL}_n^{\downarrow, \Sigma}$ to denote the restriction of $\text{LTL}^{\downarrow, \Sigma}$ to registers in $\{1, \dots, n\}$. Models of $\text{LTL}^{\downarrow, \Sigma}$ are *data words*. A data word σ over a finite alphabet Σ is a non-empty word in Σ^* or Σ^ω , together with an equivalence relation \sim^σ on word indices. We write $|\sigma|$ for the length of the data word, $\sigma(i)$ for its letters where $0 \leq i < |\sigma|$. Let $\Sigma^*(\sim)$ [resp. $\Sigma^\omega(\sim)$] denote the sets of all such finite [resp. infinite] data words. We denote by $\Sigma^\infty(\sim)$ the set $\Sigma^*(\sim) \cup \Sigma^\omega(\sim)$ of finite and infinite data words.

A *register valuation* v for a data word σ is a finite partial map from $\mathbb{N} \setminus \{0\}$ to the indices of σ . Whenever $v(r)$ is undefined, the formula \uparrow_r is interpreted as false. Let σ be a data word in $\Sigma^\infty(\sim)$ and $0 \leq i < |\sigma|$, the satisfaction relation \models is defined as follows (Boolean clauses are omitted).

$$\begin{aligned} \sigma, i \models_v a & \stackrel{\text{def}}{\iff} \sigma(i) = a \\ \sigma, i \models_v \uparrow_r & \stackrel{\text{def}}{\iff} r \in \text{dom}(v) \text{ and } v(r) \sim^\sigma i \\ \sigma, i \models_v \mathbf{X}\phi & \stackrel{\text{def}}{\iff} i + 1 < |\sigma| \text{ and } \sigma, i + 1 \models_v \phi \\ \sigma, i \models_v \phi_1 \text{U} \phi_2 & \stackrel{\text{def}}{\iff} \text{for some } i \leq j < |\sigma|, \sigma, j \models_v \phi_2 \\ & \text{and for all } i \leq j' < j, \text{ we have } \sigma, j' \not\models_v \phi_1 \\ \sigma, i \models_v \downarrow_r \phi & \stackrel{\text{def}}{\iff} \sigma, i \models_{v[r \mapsto i]} \phi \end{aligned}$$

$v[r \mapsto i]$ denotes the register valuation equal to v except that the register r is mapped to the position i . In the sequel, we omit the subscript “ v ” in \models_v when sentences are involved. We use the standard abbreviations for the temporal operators ($\mathbf{G}, \mathbf{F}, \mathbf{G}^+, \mathbf{F}^+, \dots$) and for the Boolean operators and constants ($\vee, \Rightarrow, \top, \perp, \dots$). The finitary [resp. infinitary] satisfiability problem for LTL with registers, noted $\ast\text{-SAT-LTL}^{\downarrow}$ [resp. $\omega\text{-SAT-LTL}^{\downarrow}$], is defined as follows:

Input: A finite alphabet Σ and a formula ϕ in $\text{LTL}^{\downarrow, \Sigma}$;

Question: Is there a finite [resp. an infinite] data word σ such that $\sigma, 0 \models \phi$?

Theorem 1. [8, Theorem 5.2] $\ast\text{-SAT-LTL}^{\downarrow}$ restricted to one register is decidable with non-primitive recursive complexity and $\omega\text{-SAT-LTL}^{\downarrow}$ restricted to one register is Π_1^0 -complete.

Given a one-counter automaton $\mathcal{A} = \langle Q, q_I, \delta, F \rangle$, finite [resp. infinite] accepting runs of \mathcal{A} can be viewed as finite [resp. infinite] data words over the alphabet Q . Indeed, given a run ρ , the equivalence relation \sim^ρ is defined as follows: $i \sim^\rho j$ iff the counter value at the i th position of ρ is equal to the counter value at the j th position of ρ . In order to ease the presentation, in the sequel we sometimes store counter values in registers, which is an equivalent way to proceed by slightly adapting the semantics for \uparrow_r and \downarrow_r , and the values stored in registers (data).

The finitary [resp. infinitary] (existential) model-checking problem over one-counter automata for LTL with registers, noted $\text{MC}(\text{LTL})^*$ [resp. $\text{MC}(\text{LTL})^\omega$], is defined as follows:

Input: A one-counter automaton $\mathcal{A} = \langle Q, q_I, \delta, F \rangle$ and a sentence ϕ in $\text{LTL}^{\downarrow, Q}$;

Question: Is there a finite [resp. infinite] accepting run ρ of \mathcal{A} such that $\rho, 0 \models \phi$? If the answer is “yes”, we write $\mathcal{A} \models^* \phi$ [resp. $\mathcal{A} \models^\omega \phi$].

In this existential version of model checking, this problem can be viewed as a variant of satisfiability in which satisfaction of a formula can be only witnessed within a specific class of data words, namely the accepting runs of the automata. Results for the universal version of model checking will follow easily from those for the existential version.

We write $\text{MC}(\text{LTL})_n^\alpha$ to denote the restriction of $\text{MC}(\text{LTL})^\alpha$ to formulae with at most n registers. Very often, it makes sense that only counter values are known but not the current state of a configuration, which can be understood as an internal information about the system. We write $\text{PureMC}(\text{LTL})_n^\alpha$ to denote the restriction of $\text{MC}(\text{LTL})_n^\alpha$ (its “pure data” version) to formulae with atomic formulae only of the form \uparrow_r . Given a set \mathcal{O} of temporal operators, we write $\text{MC}(\text{LTL})_n^\alpha[\mathcal{O}]$ [resp. $\text{PureMC}(\text{LTL})_n^\alpha[\mathcal{O}]$] to denote the restriction of $\text{MC}(\text{LTL})_n^\alpha$ [resp. $\text{PureMC}(\text{LTL})_n^\alpha$] to formulae using only temporal operators in \mathcal{O} .

Example 1. Here are some properties that can be stated in $\text{LTL}_2^{\downarrow, Q}$ along a run.

- “There is a suffix such that all the counter values are different”:

$$\text{FG}(\downarrow_1 \text{G}^+ \neg \uparrow_1).$$

- “Whenever state q is reached with current counter value n and next current counter value m , if there is a next occurrence of q , the two consecutive counter values are also n and m ”:

$$\text{G}(q \Rightarrow \downarrow_1 \text{X} \downarrow_2 \text{XG}(q \Rightarrow \uparrow_1 \wedge \text{X} \uparrow_2)).$$

Observe also that we have chosen as alphabet the set of states of the automata. Alternatively, it would have been possible to add finite alphabets to automata, to label each transition by a letter and then consider as data words generated from automata the recognized words augmented with the counter values. This choice does not change our main results but it improves the readability of some technical details.

2.3. First-order logic over data words

Let us introduce the second logical formalism considered in the paper. Formulae of $\text{FO}^\Sigma(\sim, <, +1)$ [7] where Σ is a finite alphabet are defined as follows:

$$\phi ::= a(\mathbf{x}) \mid \mathbf{x} \sim \mathbf{y} \mid \mathbf{x} < \mathbf{y} \mid \mathbf{x} = \mathbf{y} + 1 \mid \neg \phi \mid \phi \wedge \phi \mid \exists \mathbf{x} \phi$$

where $a \in \Sigma$ and \mathbf{x} ranges over a countably infinite set of variables. We write $\text{FO}(\sim, <, +1)$ to denote $\text{FO}^\Sigma(\sim, <, +1)$ for some unspecified finite alphabet and $\text{FO}(<, +1)$ to denote the restriction of $\text{FO}(\sim, <, +1)$ without atomic formulae of the form $\mathbf{x} \sim \mathbf{y}$. Given a natural number $n > 0$, we write $\text{FO}_n^\Sigma(\sim, <, +1)$ to denote the restriction of $\text{FO}^\Sigma(\sim, <, +1)$ to variables in $\{\mathbf{x}_1, \dots, \mathbf{x}_n\}$. A variable valuation u for a data word σ is a finite partial map from the set of variables to the indices of σ . Let σ be a data word in $\Sigma^\infty(\sim)$, the satisfaction relation \models is defined as follows (Boolean clauses are again omitted):

$$\begin{aligned} \sigma \models_u a(\mathbf{x}) &\stackrel{\text{def}}{\iff} u(\mathbf{x}) \text{ is defined and } \sigma(u(\mathbf{x})) = a \\ \sigma \models_u \mathbf{x} \sim \mathbf{y} &\stackrel{\text{def}}{\iff} u(\mathbf{x}) \text{ and } u(\mathbf{y}) \text{ are defined and } u(\mathbf{x}) \sim^\sigma u(\mathbf{y}) \\ \sigma \models_u \mathbf{x} < \mathbf{y} &\stackrel{\text{def}}{\iff} u(\mathbf{x}) \text{ and } u(\mathbf{y}) \text{ are defined and } u(\mathbf{x}) < u(\mathbf{y}) \\ \sigma \models_u \mathbf{x} = \mathbf{y} + 1 &\stackrel{\text{def}}{\iff} u(\mathbf{x}) \text{ and } u(\mathbf{y}) \text{ are defined and } u(\mathbf{x}) = u(\mathbf{y}) + 1 \\ \sigma \models_u \exists \mathbf{x} \phi &\stackrel{\text{def}}{\iff} \text{there is } i \in \mathbb{N} \text{ such that } 0 \leq i < |\sigma| \text{ and } \sigma \models_{u[\mathbf{x} \mapsto i]} \phi \end{aligned}$$

$u[\mathbf{x} \mapsto i]$ denotes the variable valuation equal to u except that the variable \mathbf{x} is mapped to the position i . In the sequel, we omit the subscript “ u ” in \models_u when sentences are involved.

The finitary [resp. infinitary] (existential) model-checking problem over one-counter automata for the logic $\text{FO}^\Sigma(\sim, <, +1)$, noted $\text{MC}(\text{FO})^*$ [resp. $\text{MC}(\text{FO})^\omega$] is defined as follows:

Input: A one-counter automaton \mathcal{A} and a sentence ϕ in $\text{FO}^Q(\sim, <, +1)$;

Question: Is there a finite [resp. infinite] accepting run ρ of \mathcal{A} such that $\rho \models \phi$? If the answer is “yes”, we write $\mathcal{A} \models^* \phi$ [resp. $\mathcal{A} \models^\omega \phi$].

We write $\text{MC}(\text{FO})_n^\alpha$ to denote the restriction of $\text{MC}(\text{FO})^\alpha$ to formulae with at most n variables. We write $\text{PureMC}(\text{FO})_n^\alpha$ to denote the restriction of $\text{MC}(\text{FO})_n^\alpha$ (its “pure data” version) to formulae with no atomic formulae of the form $a(\mathbf{x})$.

Extending the standard translation from LTL into first-order logic, we can easily establish the result below.

Lemma 2. *Given a sentence ϕ in $\text{LTL}_n^{\downarrow, \Sigma}$, there is a first-order formula ϕ' in $\text{FO}^\Sigma(\sim, <, +1)$ that can be computed in linear time in $|\phi|$ such that*

1. ϕ' has at most $\max(3, n + 1)$ variables,
2. ϕ' has a unique free variable, say \mathbf{y}_0 ,
3. for all data words σ , register valuations v and $i \geq 0$, we have $\sigma, i \models_v \phi$ iff $\sigma \models_u \phi'$, where for $r \in \{1, \dots, n\}$, $v(r) = u(\mathbf{x}_r)$ and $u(\mathbf{y}_0) = i$.

Proof. We build a translation function T which takes as arguments a formula in $\text{LTL}_n^{\downarrow, \Sigma}$ and a variable, and which returns the wanted formula in $\text{FO}^\Sigma(\sim, <, +1)$. Intuitively the variable, which is given as argument, is used to represent the current position in the data word. Then, we use the variables $\mathbf{x}_1, \dots, \mathbf{x}_r$ to characterize the registers. We add to this set of variables three variables $\mathbf{y}_0, \mathbf{y}_1$ and \mathbf{y}_2 . In the sequel, we write \mathbf{y} to represent indifferently \mathbf{y}_0 or \mathbf{y}_1 or \mathbf{y}_2 . Furthermore the notation \mathbf{y}_{i+1} stands for $\mathbf{y}_{(i+1) \bmod 3}$ and \mathbf{y}_{i+2} stands for $\mathbf{y}_{(i+2) \bmod 3}$. The function T , which is homomorphic for the Boolean operators, is defined inductively as follows, for $i \in \{0, 1, 2\}$:

- $T(a, \mathbf{y}) = a(\mathbf{y})$,
- $T(\uparrow_r, \mathbf{y}) = \mathbf{y} \sim \mathbf{x}_r$,
- $T(\mathbf{X}\phi, \mathbf{y}_i) = \exists \mathbf{y}_{i+1} (\mathbf{y}_{i+1} = \mathbf{y}_i + 1 \wedge T(\phi, \mathbf{y}_{i+1}))$,
- $T(\phi \mathbf{U} \psi, \mathbf{y}_i) = \exists \mathbf{y}_{i+1} (\mathbf{y}_i \leq \mathbf{y}_{i+1} \wedge T(\psi, \mathbf{y}_{i+1}) \wedge \forall \mathbf{y}_{i+2} (\mathbf{y}_i \leq \mathbf{y}_{i+2} < \mathbf{y}_{i+1} \Rightarrow T(\phi, \mathbf{y}_{i+2}))$,
- $T(\downarrow_r \phi, \mathbf{y}) = \exists \mathbf{x}_r (\mathbf{x}_r = \mathbf{y} \wedge T(\phi, \mathbf{y}))$.

Then if ϕ is a formula in $\text{LTL}_n^{\downarrow, \Sigma}$ and \mathbf{y}_0 is the variable chosen to characterize the current position in the word, the formula $T(\phi, \mathbf{y}_0)$ satisfies the three conditions given in the above lemma. In order to ensure the first condition, we use the fact that we can recycle the variables. More details about this technique can be found in [31]. \square

The decidability borderline for $\text{FO}(\sim, <, +1)$ is between two and three variables.

Theorem 3. [7, Theorem 1, Propositions 19 & 20] *Satisfiability for $\text{FO}(\sim, <, +1)$ restricted to 3 variables is undecidable and satisfiability for $\text{FO}_2(\sim, <, +1)$ is decidable (for both finitary and infinitary cases).*

In Section 3 we will use Theorem 4 below in an essential way.

Theorem 4. [26, Proposition 4.2] *Given two finite words $s, t \in \Sigma^*$ and a sentence ϕ in $\text{FO}^\Sigma(<, +1)$, checking whether $s \cdot t^\omega \models \phi$ can be done in space $\mathcal{O}((|s| + |t|) \times |\phi|^2)$.*

2.4. Purification of the model-checking problem

We now show how to get rid of propositional variables by reducing the model-checking problem over one-counter automata to its pure version. This amounts to transform any MC(LTL) instance into a PureMC(LTL) instance.

Lemma 5 (Purification for LTL[↓]). *Given a one-counter automaton \mathcal{A} and a sentence ϕ in LTL _{n} ^{↓,Q}, one can compute in logarithmic space in $|\mathcal{A}| + |\phi|$ a one-counter automaton \mathcal{A}_P and a formula ϕ_P in LTL _{$m \max(n,1)$} ^{↓,0} such that $\mathcal{A} \models^* \phi$ [resp. $\mathcal{A} \models^\omega \phi$] iff $\mathcal{A}_P \models^* \phi_P$ [resp. $\mathcal{A}_P \models^\omega \phi_P$]. Moreover, \mathcal{A} is deterministic iff \mathcal{A}_P is deterministic.*

The idea of the proof is simply to identify states with patterns about the changes of the unique counter that can be expressed in LTL₁^{↓,0}.

Proof. Let $\mathcal{A} = \langle Q, q_I, \delta, F \rangle$ with $Q = \{q_1, \dots, q_m\}$ and ϕ be an LTL^{↓,Q} formula. In order to define \mathcal{A}_P , we identify states with patterns about the changes of the unique counter. Let \mathcal{A}_P be $\langle Q_P, q_I, \delta_P, F_P \rangle$ with $Q_P = Q \uplus Q'$ and Q' is defined below:

$$Q' = \begin{aligned} & \{q_i^1, q_i^2, q_i^3, q_i^4, q_i^5, q_{i,F} \mid i \in \{1, \dots, m\}\} \\ & \cup \{q_{i,j}, q'_{i,j} \mid i \in \{1, \dots, m\} \text{ and } j \in \{1, \dots, m+1\} \text{ and } i \neq j\} \\ & \cup \{q_{i,i}^0, q_{i,i}, q_{i,i}^1, q_{i,i}^2 \mid i \in \{1, \dots, m\}\}. \end{aligned}$$

Figure 2 presents the set of transitions δ_P associated with each state q_i of Q (providing a pattern). Furthermore, for all $i, j \in \{1, \dots, m\}$, $q_{i,F} \xrightarrow{a} q_j \in \delta_P$ iff $q_i \xrightarrow{a} q_j \in \delta$. The sequence of transitions associated to each $q_i \in Q$ is a sequence of $m+2$ picks and among these picks, the first pick is the only one of height 3, the i -th pick is the only one of height 2, and the height of all the other picks is 1. Observe that this sequence of transitions has a fixed length and it is composed of exactly $9 + 2(m+1)$ states.

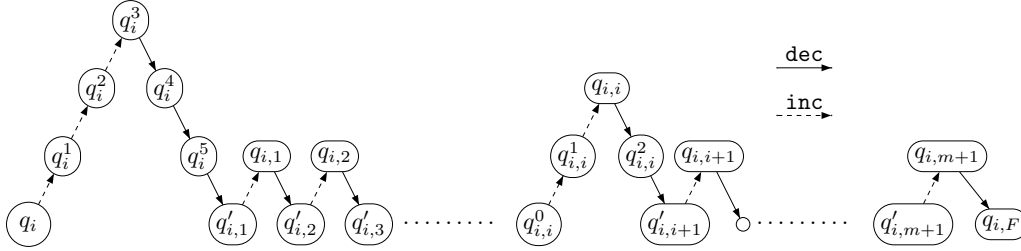


Figure 2: Encoding q_i by a pattern made of $m+2$ picks and of length $9 + 2(m+1)$

Finally, the set of accepting states of \mathcal{A}_P is defined as the set $\{q_{i,F} \mid q_i \in F\}$. In order to detect the first pick of height 3 which characterizes the beginning of the sequence of transitions associated to each state belonging to Q , we build the two following formulae in LTL₁^{↓,0}:

- $\varphi_{\sim 3/7}$ which expresses that “among the 7 next counter values (including the current counter value), there are no 3 equal values”,
- $\varphi_{0 \sim 6}$ which expresses that “the current counter value is equal to the counter value at the 6th next position”.

These two formulae can be written as follows:

$$\begin{aligned} \varphi_{\sim 3/7} &= \neg(\downarrow_1 (\bigvee_{i \neq j \in \{1, \dots, 6\}} (\mathbf{X}^i \uparrow_1 \wedge \mathbf{X}^j \uparrow_1)) \\ & \quad \vee \mathbf{X} \downarrow_1 (\bigvee_{i \neq j \in \{1, \dots, 5\}} (\mathbf{X}^i \uparrow_1 \wedge \mathbf{X}^j \uparrow_1)) \\ & \quad \vee \mathbf{X}^2 \downarrow_1 (\bigvee_{i \neq j \in \{1, \dots, 4\}} (\mathbf{X}^i \uparrow_1 \wedge \mathbf{X}^j \uparrow_1)) \\ & \quad \vee \mathbf{X}^3 \downarrow_1 (\bigvee_{i \neq j \in \{1, 2, 3\}} (\mathbf{X}^i \uparrow_1 \wedge \mathbf{X}^j \uparrow_1)) \\ & \quad \vee \mathbf{X}^4 \downarrow_1 (\bigvee_{i \neq j \in \{1, 2\}} (\mathbf{X}^i \uparrow_1 \wedge \mathbf{X}^j \uparrow_1))) \end{aligned}$$

$$\varphi_{0 \sim 6} = \downarrow_1 (\mathbf{X}^6 \uparrow_1)$$

We write STA to denote the formula $\varphi_{-3/7} \wedge \varphi_{0\sim 6}$.

Let ρ be a run of \mathcal{A}_P and j be such that $0 \leq j < |\rho|$. We show that (1) $\rho, j \models \text{STA}$ iff (2) $(\rho, j \models q$ for some $q \in Q$ and $j + 6 < |\rho|$). In the sequel, we assume that $j + 6 < |\rho|$ since otherwise it is clear that $\rho, j \not\models \text{STA}$. By construction, it is clear that (2) implies (1). In order to prove that (1) implies (2), we show that if $\rho, j \models q$ for some $q \in Q_P \setminus Q$ and $j + 6 < |\rho|$, then $\rho, j \not\models \text{STA}$. We perform a systematic case analysis according to the type of q (we group the cases that require similar arguments):

1. If q is of the form q_i^2 with $i \in \{2, \dots, m\}$, then $\rho, j \not\models \varphi_{0\sim 6}$. When q is q_1^2 , $\rho, j \not\models \varphi_{-3/7}$.
2. If q is of the form q_i^3 with $i \in \{1, \dots, m\}$, then $\rho, j \not\models \varphi_{0\sim 6}$.
3. If q is of the form q_i^4 with $i \in \{1, \dots, m\} \setminus \{2\}$, then $\rho, j \not\models \varphi_{0\sim 6}$. When q is q_2^4 , $\rho, j \not\models \varphi_{-3/7}$.
4. If q is of the form $q_{i,i}$ with $i \in \{2, \dots, m-1\}$, then $\rho, j \not\models \varphi_{0\sim 6}$. When q is $q_{m,m}$ and an incrementation is performed after $q_{m,F}$, we have $\rho, j \not\models \varphi_{-3/7}$. If another action is performed, then we also have $\rho, j \not\models \varphi_{0\sim 6}$.
5. If q is of the form either q_i^1 or q_i^5 with $i \in \{1, \dots, m\}$, then $\rho, j \not\models \varphi_{-3/7}$.
6. If q is of the form either $q_{i,i}^0$ or $q_{i,i}^1$ with $i \in \{1, \dots, m\}$, then $\rho, j \not\models \varphi_{-3/7}$.
7. If q is of the form $q_{i,i}^2$ with $i \in \{1, \dots, m\}$, then $\rho, j \not\models \varphi_{-3/7}$ (the case $i = m$ requires a careful analysis).
8. If q is of the form $q_{i,k}$ for some $i \in \{1, \dots, m\}$, $k \in \{1, \dots, m-1\}$ such that either $|i - k| > 2$ or $k > i$, then $\rho, j \not\models \varphi_{-3/7}$.
9. If q is of the form $q_{i,i-1}$ with $i \in \{2, \dots, m\}$, then $\rho, j \not\models \varphi_{-3/7}$.
10. If q is of the form $q_{i,i-2}$ with $i \in \{3, \dots, m\}$, then $\rho, j \not\models \varphi_{-3/7}$.
11. If q is of the form $q_{i,m}$ with $i \in \{1, \dots, m-1\}$, then $\rho, j \not\models \varphi_{-3/7}$.
12. If q is of the form $q_{i,m+1}$ with $i \in \{1, \dots, m\}$ and an action different from decrementation is performed after $q_{i,F}$, then $\rho, j \not\models \varphi_{0\sim 6}$. When a decrementation is performed after $q_{i,F}$, we get $\rho, j \models \varphi_{0\sim 6} \wedge \neg \varphi_{-3/7}$.
13. If q is of the form $q'_{i,k}$ for some $i \in \{1, \dots, m\}$, $k \in \{1, \dots, m-1\}$ such that either $|i - k| > 2$ or $k > i$, then $\rho, j \not\models \varphi_{-3/7}$.
14. If q is of the form $q'_{i,i-1}$ with $i \in \{2, \dots, m\}$, then $\rho, j \not\models \varphi_{-3/7}$.
15. If q is of the form $q'_{i,i-2}$ with $i \in \{3, \dots, m\}$, then $\rho, j \not\models \varphi_{-3/7}$.
16. If q is of the form $q'_{i,m}$ with $i \in \{1, \dots, m\}$, then $\rho, j \not\models \varphi_{-3/7}$.
17. If q is of the form $q'_{i,m+1}$ with $i \in \{1, \dots, m\}$, then $\rho, j \not\models \varphi_{0\sim 6}$. Indeed, the 6th next position, if any, is of the form q_k^3 for some $k \in \{1, \dots, m\}$. The counter value at such a position is strictly greater than the one at the position j whatever is the action performed after $q_{i,F}$.
18. If q is of the form $q_{i,F}$ with $i \in \{1, \dots, m\}$ and the action performed after $q_{i,F}$ is not a decrementation, then $\rho, j \not\models \varphi_{0\sim 6}$. When a decrementation is performed after $q_{i,F}$, we get $\rho, j \models \varphi_{0\sim 6} \wedge \neg \varphi_{-3/7}$.

For $i \in \{1, \dots, m\}$, let us define the formula $\phi_i = \mathbf{x}^{6+2(i-1)} \downarrow_1 \mathbf{x}^2 \neg \uparrow_1$. One can check that in the run of \mathcal{A}_P , $\text{STA} \wedge \phi_i$ holds true iff the current state is q_i and there are at least 6 following positions.

Let ϕ be a formula in $\text{LTL}_n^{+,Q}$. We define ϕ_P as the formula $\text{T}(\phi)$ such that the map T is homomorphic for Boolean operators and \downarrow_r , and its restriction to \uparrow_r is identity. The rest of the inductive definition is as follows.

- $\text{T}(q_i) = \phi_i$,
- $\text{T}(\mathbf{X}\phi) = \mathbf{x}^{9+2(m+1)+1}\text{T}(\phi)$,
- $\text{T}(\phi \cup \phi') = (\text{STA} \Rightarrow \text{T}(\phi)) \cup (\text{STA} \wedge \text{T}(\phi'))$.

Observe that ϕ and ϕ_P have the same amount of registers unless ϕ has no register. For each accepting run in \mathcal{A} , there exists an accepting run in \mathcal{A}_P and conversely for each accepting run in \mathcal{A}_P , there exists an accepting run in \mathcal{A} . Furthermore the sequence of counter values for the configurations of each of these runs which have a state in Q match. \square

Lemma 6 (Purification for $\text{FO}(\sim, <, +1)$). *Given a one-counter automaton \mathcal{A} and an $\text{FO}^Q(\sim, <, +1)$ sentence ϕ with n variables, one can compute in logarithmic space in $|\mathcal{A}| + |\phi|$ a one-counter automaton \mathcal{A}_P and ϕ_P in $\text{FO}^0(\sim, <, +1)$ with at most $n + 2$ variables such that $\mathcal{A} \models^* \phi$ [resp. $\mathcal{A} \models^\omega \phi$] iff $\mathcal{A}_P \models^* \phi_P$ [resp. $\mathcal{A}_P \models^\omega \phi_P$]. Moreover, \mathcal{A} is deterministic iff \mathcal{A}_P is deterministic.*

Proof. The proof follows the lines of the proof of Lemma 5 by considering the first-order formulae corresponding to the formulae STA and ϕ_i and the same automaton construction. In order to make this construction feasible, we need to use formulae of the form $x = y + k$. In fact, the formulae of the form $x = y + 1$ are translated into formulae of the form $x = y + 9 + 2(m + 1)$ (this case is identical to the case of the formulae of the form $X\phi$). Typically, encoding $x = y + k$ for the constant k requires two auxiliary variables. For instance we can encode the formula $x = y + 4$ as follows:

$$\exists y_2 \ x = y_2 + 1 \wedge (\exists y_1 \ y_2 = y_1 + 1 \wedge (\exists y_2 \ y_1 = y_2 + 1 \wedge y_2 = y + 1))$$

Here again, we recycle the variables y_1 and y_2 . □

3. Model checking deterministic one-counter automata

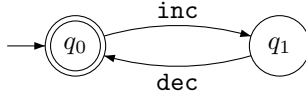
In this section, we show that $\text{MC}(\text{LTL})^*$ and $\text{MC}(\text{LTL})^\omega$ restricted to deterministic one-counter automata is PSPACE-complete.

3.1. PSPACE lower bound

We show below a PSPACE-hardness result by taking advantage of the alphabet of states by means of a reduction from QBF (“Quantified Boolean Formula”) that is a standard PSPACE-complete problem.

Proposition 7. *PureMC(LTL)^{*} and PureMC(LTL)^ω restricted to deterministic one-counter automata are PSPACE-hard problems. Furthermore, for PureMC(LTL)^{*} [resp. PureMC(LTL)^ω] this results holds for formulae using only the temporal operators X and F [resp. F].*

Proof. Consider a QBF instance ϕ : $\phi = \forall p_1 \exists p_2 \cdots \forall p_{2N-1} \exists p_{2N} \Psi(p_1, \dots, p_{2N})$ where p_1, \dots, p_{2N} are propositional variables and $\Psi(p_1, \dots, p_{2N})$ is a quantifier-free propositional formula built over p_1, \dots, p_{2N} . The fixed deterministic one-counter automaton \mathcal{A} below generates the sequence of counter values $(01)^\omega$.



Let ψ be the formula in $\text{LTL}^{\downarrow, \emptyset}$ defined from the family $\psi_1, \dots, \psi_{2N+1}$ of formulae with $\psi = \downarrow_{2N+1} \psi_1$.

- $\psi_{2N+1} = \Psi(\uparrow_1 \Leftrightarrow \uparrow_{2N+1}, \dots, \uparrow_{2N} \Leftrightarrow \uparrow_{2N+1})$,
- for $i \in \{1, \dots, N\}$, $\psi_{2i} = \mathbf{F}(\downarrow_{2i} \psi_{2i+1})$ and $\psi_{2i-1} = \mathbf{G}(\downarrow_{2i-1} \psi_{2i})$.

One can show that ϕ is satisfiable iff $\mathcal{A} \models^\omega \psi$.

To do so, we proceed as follows. For $i \in \{0, 2, 4, 6, \dots, 2N\}$, let ϕ_i be

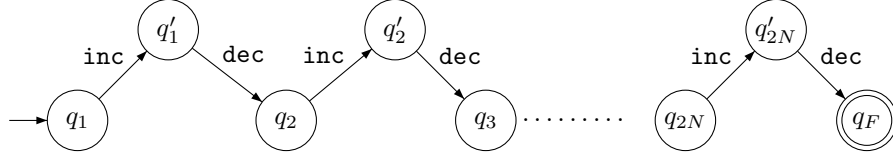
$$\phi_i = \forall p_{i+1} \exists p_{i+2} \cdots \forall p_{2N-1} \exists p_{2N} \Psi(p_1, \dots, p_{2N}).$$

So ϕ_0 is precisely ϕ . Similarly, for $i \in \{1, 3, 5, \dots, 2N-1\}$, let ϕ_i be

$$\phi_i = \exists p_{i+1} \forall p_{i+2} \cdots \forall p_{2N-1} \exists p_{2N} \Psi(p_1, \dots, p_{2N}).$$

Observe that the free propositional variables in ϕ_i are exactly p_1, \dots, p_i and ϕ_i is obtained from ϕ by removing the i first quantifications. Given a propositional valuation $v : \{p_1, \dots, p_i\} \rightarrow \{\top, \perp\}$ for some $i \in \{1, \dots, 2N\}$, we write \bar{v} to denote a register valuation such that its restriction to $\{1, \dots, i, 2N+1\}$ satisfies: $v(p_j) = \top$ iff $\bar{v}(j) = 0$ for $j \in \{1, \dots, i\}$ and $\bar{v}(2N+1) = 0$. One can show by induction that for $k \geq 0$, $v \models \phi_{i-1}$ (in QBF) iff $\rho_{\mathcal{A}}^\omega, k \models_{\bar{v}} \psi_i$, where $\rho_{\mathcal{A}}^\omega$ denotes the unique infinite run for \mathcal{A} . Consequently, if $v \models \phi$ for some propositional valuation, then $\rho_{\mathcal{A}}^\omega, 0 \models_{\bar{v}} \psi$. Similarly, if $\rho_{\mathcal{A}}^\omega, 0 \models_v \psi$, then there is a propositional valuation v' such that $\bar{v}' = v$ and $v' \models \phi$.

For the finitary problem $\text{PureMC}(\text{LTL})^*$, the above proof does not work because the occurrences of \mathbf{G} related to universal quantification in the QBF formula might lead to the end of the run, leaving no choice for the next quantifications. Consequently, one need to use another deterministic one-counter automaton with $4N + 1$ states such that the sequence of counter values from the accepting run is $(01)^{2N}0$ (again we omit useless **ifzero** transitions). Let us consider the deterministic counter automaton \mathcal{A}' below.



We shall build another formula ψ in $\text{LTL}^{\downarrow, \emptyset}$ defined from the formulae below with $\psi = \downarrow_{2N+1} \psi_1$.

- $\psi_{2N+1} = \Psi(\uparrow_1 \Leftrightarrow \uparrow_{2N+1}, \dots, \uparrow_{2N} \Leftrightarrow \uparrow_{2N+1})$,
- for $i \in \{1, \dots, N\}$:
 - $\psi_{2i} = \mathbf{F}((\mathbf{X}^{4N-4i+2} \top) \wedge \downarrow_{2i} \psi_{2i+1})$ and
 - $\psi_{2i-1} = \mathbf{G}((\mathbf{X}^{4N-4i+4} \top) \Rightarrow \downarrow_{2i-1} \psi_{2i})$.

Herein, \top holds for the truth value that can be encoded with $\downarrow_1 \vee \neg \downarrow_1$ (remember there are no propositional variables in the pure version of the model-checking problems).

Using a similar proof by induction as the one done for the infinite case, we obtain that ϕ is satisfiable iff $\mathcal{A}' \models^* \psi$. \square

Observe that in the reduction for $\text{PureMC}(\text{LTL})^\omega$, we use an unbounded number of registers (see Theorem 14) but a fixed deterministic one-counter automaton.

By Lemmas 6 and 2, we obtain the following corollary.

Corollary 8. *PureMC(FO)* and PureMC(FO) $^\omega$ restricted to deterministic one-counter automata are PSPACE-hard problems.*

3.2. Properties on runs for deterministic automata

Any deterministic one-counter automaton \mathcal{A} has at most one infinite run, possibly with an infinite amount of counter values. If this run is not accepting, i.e. no accepting state is repeated infinitely often, then for no formula ϕ , we have $\mathcal{A} \models^\omega \phi$. We show below that we can decide in polynomial-time whether \mathcal{A} has accepting runs either finite or infinite. Moreover, we shall show that the infinite unique run has some regularity.

Let $\rho_{\mathcal{A}}^\omega$ be the unique infinite run (if it exists) of the deterministic one-counter automaton \mathcal{A} represented by the following sequence of configurations

$$\langle q_0, n_0 \rangle \langle q_1, n_1 \rangle \langle q_2, n_2 \rangle \dots$$

Lemma 9 below is a key result to show the forthcoming PSPACE upper bound. Basically, the unique run of deterministic one-counter automata has regularities that can be described in polynomial size.

Lemma 9. *Let \mathcal{A} be a deterministic one-counter automaton with an infinite run. There are K_1, K_2, K_{inc} such that $K_1 + K_2 \leq |Q|^3$, $K_{inc} \leq |Q|$ and for every $i \geq K_1$, $\langle q_{i+K_2}, n_{i+K_2} \rangle = \langle q_i, n_i + K_{inc} \rangle$.*

Hence, the run $\rho_{\mathcal{A}}^\omega$ can be encoded by its first $K_1 + K_2$ configurations. It is worth noting that we have deliberately decided to keep the three constants K_1 , K_2 and K_{inc} in order to provide a more explicit analysis.

Proof. (Lemma 9) We write $\text{ZERO}(\mathcal{A})$ to denote the set of positions of $\rho_{\mathcal{A}}^\omega$ where a zero-test has been successful. By convention, 0 belongs to $\text{ZERO}(\mathcal{A})$ since in a run we require that the first configuration is the initial configuration of \mathcal{A} with counter value 0. Hence, $\text{ZERO}(\mathcal{A}) \stackrel{\text{def}}{=} \{0\} \cup \{i > 0 : n_i = n_{i+1} = 0\}$. Let us first establish Lemma 10 below.

Lemma 10. *Let $i < j$ be in $\text{ZERO}(\mathcal{A})$ for which there is no $i < k < j$ with $k \in \text{ZERO}(\mathcal{A})$. Then, $(j - i) \leq |Q|^2$.*

The proof essentially establishes that the counter cannot go beyond $|Q|$ between two positions with successful zero-tests.

Proof. (Lemma 10) First observe that there are no $i < k < k' < j$ such that $q_k = q_{k'}$ and $n_k \leq n_{k'}$. Indeed, if it is the case since there is no successful zero-tests in $\langle q_{i+1}, n_{i+1} \rangle \cdots \langle q_k, n_k \rangle \cdots \langle q_{k'}, n_{k'} \rangle$ and \mathcal{A} is deterministic we would obtain from $\langle q_{k'}, n_{k'} \rangle$ an infinite path with no zero-test, a contradiction with the existence of $\langle q_j, n_j \rangle$. Hence, if there are $i < k < k' < j$ such that $q_k = q_{k'}$, then $n_{k'} < n_k$. Now suppose that there is $i < k < j$ such that $n_k \geq |Q|$. We can extract a subsequence $\langle q_{i_0}, n_{i_0} \rangle \cdots \langle q_{i_s}, n_{i_s} \rangle$ from $\langle q_i, n_i \rangle \cdots \langle q_{n_k}, n_k \rangle$ such that $i_0 = i$, $i_s = k$ and for $0 \leq l < s$, $n_{i_{l+1}} = n_{i_l} + 1$. Consequently, there are l, l' such that $q_{i_l} = q_{i_{l'}}$ and $n_{i_l} < n_{i_{l'}}$, which leads to a contradiction from the above point. Hence, for $k \in \{i, \dots, j\}$, $n_k \leq |Q| - 1$. Since \mathcal{A} is deterministic, this implies that $(j - i) \leq |Q| \times |Q|$. \square

Let us come back to the rest of the proof.

First, suppose that $\text{ZERO}(\mathcal{A})$ is infinite. Let $i_0 < i_1 < i_2 < \dots$ be the infinite sequence composed of elements from $\text{ZERO}(\mathcal{A})$ ($i_0 = 0$). There are $l, l' \leq |Q|$ such that $\langle q_{i_l}, n_{i_l} \rangle = \langle q_{i_{l'}}, n_{i_{l'}} \rangle$. By Lemma 10, $i_{l'} \leq |Q| \times |Q|^2$. Take $K_1 = i_l$ and $K_2 = i_{l'} - i_l$.

Second, suppose that $\text{ZERO}(\mathcal{A})$ is finite, say equal to $\{0, i_1, \dots, i_l\}$ for some $l \leq |Q| - 1$ (if $l \geq |Q|$ we are in the first case). By Lemma 10, $i_l \leq (|Q| - 1) \times |Q|^2$. For all $i_l \leq k < k'$, if $q_k = q_{k'}$, then $n_k \leq n_{k'}$ (if it were not the case, there would eventually be another zero-test in the path starting with $\langle q_{i_l}, n_{i_l} \rangle$). Now there are $i_l \leq k < k' \leq i_l + |Q|$ such that $q_k = q_{k'}$ and consequently $n_k \leq n_{k'}$. Take $K_1 = k$, $K_2 = k' - k$ and $K_{inc} = n_{k'} - n_k$. We have $K_{inc} \leq |Q|$ because $k' - k \leq |Q|$. \square

$\rho_{\mathcal{A}}^\omega$ has a simple structure: it is composed of a polynomial-size prefix

$$\langle q_0, n_0 \rangle \cdots \langle q_{K_1-1}, n_{K_1-1} \rangle$$

followed by the polynomial-size loop $\langle q_{K_1}, n_{K_1} \rangle \cdots \langle q_{K_1+K_2-1}, n_{K_1+K_2-1} \rangle$ repeated infinitely often. The effect of applying the loop consists in adding K_{inc} to every counter value. Testing whether \mathcal{A} has an infinite run or $\rho_{\mathcal{A}}^\omega$ is accepting amounts to check whether there is an accepting state in the loop, which can be done in cubic time in $|Q|$. In the rest of this section, we assume that $\rho_{\mathcal{A}}^\omega$ is accepting. Similarly, testing whether \mathcal{A} has a finite accepting run amounts to check whether an accepting state occurs in the prefix or in the loop.

When $K_{inc} = 0$ and \mathcal{A} has an infinite run, $\rho_{\mathcal{A}}^\omega$ is exactly

$$\langle q_0, n_0 \rangle \cdots \langle q_{K_1-1}, n_{K_1-1} \rangle (\langle q_{K_1}, n_{K_1} \rangle \cdots \langle q_{K_1+K_2-1}, n_{K_1+K_2-1} \rangle)^\omega.$$

It is then possible to apply a polynomial-space labelling algorithm à la CTL for model checking $\text{LTL}^{\downarrow, Q}$ formulae on \mathcal{A} . However, one needs to take care of register valuations, which explains why unlike the polynomial-time algorithm for model checking ultimately periodic models on LTL formulae (see e.g., [26]), model checking restricted to deterministic automata with $K_{inc} = 0$ is still PSPACE-hard (see the proof of Proposition 7).

3.3. A PSPACE symbolic model-checking algorithm

In this section, we provide decision procedures for solving $\text{MC}(\text{FO})^*$ and $\text{MC}(\text{FO})^\omega$ restricted to deterministic one-counter automata. Let us introduce some notations. Let $\rho_{\mathcal{A}}^\omega = \langle q_0, n_0 \rangle \langle q_1, n_1 \rangle \langle q_2, n_2 \rangle \dots$ be the unique run of the deterministic one-counter automaton \mathcal{A} .

We establish that whenever $K_{inc} > 0$, two positions with identical counter values are separated by a distance that is bounded by a polynomial in $|Q|$.

Let us introduce a few constants related to the one-counter automaton \mathcal{A} when $K_{inc} > 0$.

- Let $\beta_1, \beta_2 \geq 0$ be the smallest natural numbers such that for every $i \in [K_1, K_1 + K_2 - 1]$, $n_i \in [n_{K_1} - \beta_1, n_{K_1} + \beta_2]$.
- Let γ be the greatest value amongst $\{n_0, \dots, n_{K_1-1}\}$.
- $L = 1 + \gamma + \left\lceil \frac{\beta_1 + \beta_2}{K_{inc}} \right\rceil$ where $\lceil \cdot \rceil$ denotes the ceiling function.

Intuitively, the constant LK_2 is greater than any distance between two positions belonging to the loop of the unique infinite run of \mathcal{A} which have the same counter value. The next lemma formalizes this idea.

Lemma 11. *Suppose $K_{inc} > 0$ and let i, j be in \mathbb{N} .*

1. *If $i, j \geq K_1$ and $|i - j| \geq LK_2$, then $n_i \neq n_j$.*
2. *If $i < K_1$ and $j \geq K_1 + LK_2$, then $n_i \neq n_j$.*

Proof. (1) Assume that $i, j \geq K_1$ and $(i - j) \geq LK_2$. By using the Euclidean division, we introduce the following values: $r_i = (i - K_1) \bmod (K_2)$, $r_j = (j - K_1) \bmod (K_2)$ and the quotients a_i and a_j such that $i - K_1 = a_i K_2 + r_i$ and $j - K_1 = a_j K_2 + r_j$. Note that $0 \leq r_i, r_j < K_2$ and since $(i - j) \geq LK_2$, we necessarily have $a_i - a_j > L - 1$. Using the definition of the constants β_1 and β_2 , we know that $n_{r_i + K_1}, n_{r_j + K_1} \in \{n_{K_1 - \beta_1}, \dots, n_{K_1 + \beta_2}\}$. Since $i = a_i K_2 + r_i + K_1$ and $j = a_j K_2 + r_j + K_1$, by Lemma 9, we have $n_i = n_{r_i + K_1} + a_i K_{inc}$ and $n_j = n_{r_j + K_1} + a_j K_{inc}$. We obtain the following inequalities:

$$\begin{aligned} n_{K_1 - \beta_1} + a_i K_{inc} &\leq n_i \leq n_{K_1 + \beta_2} + a_i K_{inc} \\ n_{K_1 - \beta_1} + a_j K_{inc} &\leq n_j \leq n_{K_1 + \beta_2} + a_j K_{inc} \end{aligned}$$

Consequently,

$$-\beta_1 - \beta_2 + (a_i - a_j)K_{inc} \leq n_i - n_j \leq \beta_1 + \beta_2 + (a_i - a_j)K_{inc}$$

Considering that $(a_i - a_j) > L - 1$ and using the definition of L , we obtain:

$$0 \leq \gamma K_{inc} < n_i - n_j$$

Hence $n_i \neq n_j$. The same proof can be done when we initially assume that $(j - i) \geq LK_2$.

(2) Let us assume that $i < K_1$ and $j \geq K_1 + LK_2$. Let a_j, r_j be defined as for the case (1). By using the same method, we obtain the following inequality:

$$n_{K_1 - \beta_1} + a_j K_{inc} \leq n_j \leq n_{K_1 + \beta_2} + a_j K_{inc}$$

Sine $\beta_2 \geq 0$, we have:

$$n_{K_1 - \beta_1} - \beta_1 - \beta_2 + a_j K_{inc} - n_i \leq n_j - n_i$$

Moreover, since $j \geq K_1 + LK_2$, we get $a_j \geq L$. Consequently,

$$n_{K_1 - \beta_1} - \beta_1 - \beta_2 + LK_{inc} - n_i \leq n_j - n_i$$

Using the definition of L , we get

$$n_{K_1 - \beta_1} - \beta_1 - \beta_2 + (1 + \gamma)K_{inc} + \beta_1 + \beta_2 - n_i \leq n_{K_1 - \beta_1} - \beta_1 - \beta_2 + LK_{inc} - n_i \leq n_j - n_i$$

Since $\gamma \times K_{inc} \geq n_i$, we get

$$n_{K_1} + K_{inc} \leq n_j - n_i$$

Consequently, $n_j > n_i$. □

Let us introduce the intermediate sets P_{\sim}^1 and P_{\sim}^2 :

$$\begin{aligned} P_{\sim}^1 &= \{\langle i, j \rangle \in \{0, \dots, K_1 + LK_2 - 1\}^2 \mid n_i = n_j \text{ and } i \leq j\} \\ P_{\sim}^2 &= \{\langle i, j \rangle \in \{0, \dots, K_1 + LK_2 - 1\}^2 \mid n_i = n_j + LK_{inc} \text{ and } j < i\} \end{aligned}$$

In the sequel, we write P_{\sim} to denote the set $P_{\sim}^1 \cup P_{\sim}^2$. We will now characterize the positions of $\rho_{\mathcal{A}}^{\omega}$ using the set P_{\sim} and the constants L, K_1, K_2 and K_{inc} introduced before.

Lemma 12. *Suppose $K_{inc} > 0$ and let $j \geq i$ be in \mathbb{N} . Then, $n_i = n_j$ iff one the conditions below is true.*

1. $\langle i, j \rangle \in P_{\sim}^1$.
2. $i, j \geq K_1$, $\langle K_1 + (i - K_1) \bmod (LK_2), K_1 + (j - K_1) \bmod (LK_2) \rangle \in P_{\sim}$ and $(j - i) < LK_2$.

Proof. Let $i, j \in \mathbb{N}$ such that $i \leq j$. If (1) is satisfied, then by definition of P_{\sim}^1 , we get $n_i = n_j$.

If (2) is satisfied, then let $r_i = (i - K_1) \bmod (LK_2)$, $r_j = (j - K_1) \bmod (LK_2)$ and a_i, a_j be quotients such that $i - K_1 = a_i LK_2 + r_i$ and $j - K_1 = a_j LK_2 + r_j$. By Lemma 9, we have $n_i = n_{r_i + K_1 + a_i LK_2} = n_{r_i + K_1} + a_i LK_{inc}$ and $n_j = n_{r_j + K_1 + a_j LK_2} = n_{r_j + K_1} + a_j LK_{inc}$. Since $(j - i) < LK_2$, we have $(a_j - a_i) LK_2 + (r_j - r_i) < LK_2$. Furthermore, we have by hypothesis $\langle K_1 + r_i, K_1 + r_j \rangle \in P_{\sim}$. We then distinguish two cases. First if $\langle K_1 + r_i, K_1 + r_j \rangle \in P_{\sim}^1$, we deduce that $r_i \leq r_j$ and consequently $a_i = a_j$. Hence $n_i = n_j$. Second if $\langle K_1 + r_i, K_1 + r_j \rangle \in P_{\sim}^2$, we deduce that $r_j < r_i$ and consequently $a_j = a_i + 1$. Hence $n_j = n_{r_j + K_1} + (a_i + 1) LK_{inc}$ and since $n_{r_j + K_1} + LK_{inc} = n_{r_i + K_1}$, we obtain $n_i = n_j$.

We now suppose that $n_i = n_j$ and we perform the following case analysis.

- Assume that $i < K_1$ and $j < K_1$. By definition of P_{\sim}^1 , we have $\langle i, j \rangle \in P_{\sim}^1$ and the condition (1) is therefore satisfied.
- Assume that $i, j \geq K_1$. By Lemma 11, we have $(j - i) < LK_2$ (otherwise we would have $n_i \neq n_j$). Let $r_i = (i - K_1) \bmod (LK_2)$, $r_j = (j - K_1) \bmod (LK_2)$ and a_i, a_j be quotients such that $i - K_1 = a_i LK_2 + r_i$ and $j - K_1 = a_j LK_2 + r_j$. By Lemma 9, we have $n_i = n_{r_i + K_1 + a_i LK_2} = n_{r_i + K_1} + a_i LK_{inc}$ and $n_j = n_{r_j + K_1 + a_j LK_2} = n_{r_j + K_1} + a_j LK_{inc}$. We consider then two cases, according to the satisfaction of $a_i = a_j$.
 - Suppose $a_i = a_j$. Consequently, $n_{r_i + K_1} = n_{r_j + K_1}$ and since $i \leq j$, we have $r_i \leq r_j$. Condition (2) is therefore satisfied.
 - Suppose $a_i \neq a_j$. Since $(j - i) < LK_2$, necessarily, $a_j = a_i + 1$. Hence $n_{r_j + K_1} = n_i - (a_i + 1) LK_{inc}$, and since $(a_j - a_i) LK_2 + (r_j - r_i) < LK_2$, we also have $r_j < r_i$ from which we can conclude that condition (2) is again satisfied (we also have $n_{r_j + K_1} + LK_{inc} = n_{r_i + K_1}$).
- Assume that $i < K_1$ and $j \geq K_1$. By Lemma 11, we have $j < K_1 + LK_2$, and consequently $\langle i, j \rangle \in P_{\sim}^1$, hence condition (1) is satisfied.

All the values for i, j are covered by the above analysis. \square

We show below how to reduce an instance of the model-checking problem (restricted to deterministic one-counter automata) to an instance of the problem mentioned in Theorem 4 by taking advantage of Lemma 12. First let us build finite words s, t over some finite alphabet Σ . By Lemma 6, we can assume that the formula ϕ belongs to the pure fragment of $\text{FO}(\sim, <, +1)$.

- $\Sigma = \{0, \dots, K_1 + LK_2 - 1\}$.
- $s = \{0\} \cdot \{1\} \cdots \{K_1 - 1\}$.
- $t = \{K_1\} \cdot \{K_1 + 1\} \cdots \{K_1 + LK_2 - 1\}$.

Given a sentence ϕ in $\text{FO}(\sim, <, +1)$ let us define a sentence $T(\phi)$ in $\text{FO}^{\Sigma}(<, +1)$ according to the definition below:

- T is the identity for atomic formulae of the form $\mathbf{x} < \mathbf{y}$ and $\mathbf{x} = \mathbf{y} + 1$.
- T is homomorphic for Boolean connectives and first-order quantification.
- $T(\mathbf{x} \sim \mathbf{y}) = (\mathbf{x} \leq \mathbf{y} \wedge T_1(\mathbf{x}, \mathbf{y})) \vee (\mathbf{y} \leq \mathbf{x} \wedge T_1(\mathbf{y}, \mathbf{x}))$ and $T_1(\mathbf{x}, \mathbf{y})$ is equal to

$$(\mathbf{y} - \mathbf{x}) < LK_2 \wedge (\mathbf{x} < K_1 \Rightarrow \bigvee_{\langle I, J \rangle \in P_{\sim}^1} I(\mathbf{x}) \wedge J(\mathbf{y})) \wedge (\mathbf{x} \geq K_1 \Rightarrow \bigvee_{\langle I, J \rangle \in P_{\sim}} I(\mathbf{x}) \wedge J(\mathbf{y}))$$

Observe that the formula of the form $(y - x) < LK_2$ is a shortcut for a formula in $\text{FO}^Q(<, +1)$ of polynomial size in $|\mathcal{A}|$. For instance, when $x \geq K_1 \wedge y \geq K_1 \wedge y > x$ holds, $(y - x) < LK_2$ is equivalent to a formula with at most 3 variables, namely

$$\neg \bigwedge_{I=K_1}^{K_1+LK_2-1} \exists z \ x \leq z < y \wedge I(z).$$

Lemma 13. $\mathcal{A} \models^\omega \phi$ iff $s \cdot t^\omega \models T(\phi)$.

Proof. The proof is by structural induction. We show that for each subformula ψ of ϕ and for each variable valuation u , $\mathcal{A} \models_u^\omega \psi$ iff $s \cdot t^\omega \models_u T(\psi)$. Since the formula ϕ belongs to the pure fragment of $\text{FO}(\sim, <, +1)$ the only case that needs to be checked is for atomic formulae of the form $x \sim y$. Before giving the rest of the proof, we remark that since σ is an infinite word $s \cdot t^\omega$ built over the alphabet $\Sigma = \{0, \dots, K_1 + LK_2 - 1\}$, for all $i \geq K_1$, we have $\sigma(i) = K_1 + (i - K_1) \bmod (LK_2)$. Let u be a variable valuation such that $u(x)$ and $u(y)$ are defined (if $u(x)$ or $u(y)$ is not defined, then it is easy to show that $\mathcal{A} \not\models_u^\omega x \sim y$ and that $s \cdot t^\omega \not\models_u T(x \sim y)$).

First we suppose that $\mathcal{A} \models_u^\omega x \sim y$, this means that the unique infinite accepting run $\rho_{\mathcal{A}}^\omega$ of \mathcal{A} satisfies $\rho_{\mathcal{A}}^\omega \models_u x \sim y$. Hence, we have $n_{u(x)} = n_{u(y)}$. We show that $s \cdot t^\omega \models_u T(x \sim y)$. We suppose $u(x) \leq u(y)$ (the proof is similar for the case $u(y) \leq u(x)$). We proceed by a case analysis using Lemma 12 and the definition for $T(x \sim y)$:

- If $u(x) < K_1$, then necessarily $(u(y) - u(x)) < LK_2$, hence $\sigma(u(x)) = u(x)$ and $\sigma(u(y)) = u(y)$, furthermore by Lemma 12 $\langle u(x), u(y) \rangle \in P_{\sim}^1$, so we have $\sigma \models_u T(x \sim y)$.
- If $u(x) \geq K_1$, again we have $(u(y) - u(x)) < LK_2$ and also $\sigma(u(x)) = K_1 + (i - u(x)) \bmod (LK_2)$ and $\sigma(u(y)) = K_1 + (i - u(y)) \bmod (LK_2)$. Using Lemma 12, we have $\langle \sigma(u(x)), \sigma(u(y)) \rangle \in P_{\sim}$, which implies $\sigma \models_u T(x \sim y)$.

Now, let us suppose that $s \cdot t^\omega \not\models_u T(x \sim y)$. Again, we perform a case analysis and we suppose that $u(x) \leq u(y)$ (the proof for the case $u(y) \leq u(x)$ is the same):

- If $u(x) < K_1$ then $u(y) < K_1 + LK_2$. Hence $\sigma(u(x)) = u(x)$ and $\sigma(u(y)) = u(y)$. Since $\langle u(x), u(y) \rangle \in P_{\sim}^1$, we have $n_{u(x)} = n_{u(y)}$.
- If $u(x) \geq K_1$ then $(u(y) - u(x)) < LK_2$ and $\langle \sigma(u(x)), \sigma(u(y)) \rangle \in P_{\sim}$. Since $\sigma(u(x)) = K_1 + (i - u(x)) \bmod (LK_2)$ and $\sigma(u(y)) = K_1 + (i - u(y)) \bmod (LK_2)$, we obtain using Lemma 12 that $n_{u(x)} = n_{u(y)}$.

□

This allows us to characterize the complexity of model checking.

Theorem 14. $\text{MC}(\text{FO})^\omega$ restricted to deterministic one-counter automata is PSPACE-complete.

Proof. Let \mathcal{A} be a one-counter automaton and ϕ be a pure formula in $\text{FO}(\sim, <, +1)$. If either \mathcal{A} has no infinite run or its infinite run is not accepting, then this can be checked in polynomial-time in $|\mathcal{A}|$. In that case $\mathcal{A} \not\models^\omega \phi$ does not hold. Moreover, observe that if \mathcal{A} has no infinite run, then the length of the maximal finite run is in $\mathcal{O}(|Q|^3)$ by using arguments from Lemma 9.

In the case \mathcal{A} has an infinite accepting run and $K_{inc} > 0$, as shown previously the prefixes s, t as well as the formula $T(\phi)$ can be computed in polynomial time in $|\mathcal{A}| + |\phi|$. Moreover, by Theorem 4 [26], $s \cdot t^\omega \models T(\phi)$ can be checked in polynomial space in $|s| + |t| + |T(\phi)|$. In the case $K_{inc} = 0$, the prefixes s and t are defined as follows with $\Sigma = \{0, \dots, K_1 + K_2 - 1\}$: $s = \{0\} \cdot \{1\} \cdots \{K_1 - 1\}$ and $t = \{K_1\} \cdot \{K_1 + 1\} \cdots \{K_1 + K_2 - 1\}$. The map $T(\cdot)$ is defined as previously except that $T(x \sim y) = \bigvee_{\langle I, J \rangle \in P_{\sim}^3} I(x) \wedge J(y)$ with $P_{\sim}^3 = \{\langle i, j \rangle \in \{0, \dots, K_1 + K_2 - 1\}^2 \mid n_i = n_j\}$.

Hence, $\text{PureMC}(\text{FO})^\omega$ is in polynomial space. Using the Purification Lemma 6, we deduce that $\text{MC}(\text{FO})^\omega$ is also in polynomial space. The PSPACE-hardness is a consequence of the PSPACE-hardness of $\text{MC}(\text{LTL})^\omega$ (since there is an obvious logspace translation from LTL^Q into $\text{FO}^Q(\sim, <, +1)$). □

Theorem 15. $\text{MC}(\text{FO})^*$ restricted to deterministic one-counter automata is PSPACE-complete.

Proof. Let \mathcal{A} be a one-counter automaton and ϕ be a pure formula in $\text{FO}(\sim, <, +1)$. If \mathcal{A} has an infinite run, then the finite words s and t are computed as in the infinitary case. We then need another intermediate set P_F which will characterize the positions of the unique run labelled with an accepting state:

$$P_F = \{i \in \{0, \dots, K_1 + LK_2 - 1\} \mid q_i \in F\}$$

The pure formula ϕ is then translated into

$$\exists \mathbf{x}_{end} \left(\bigvee_{I \in P_F} I(\mathbf{x}_{end}) \right) \wedge T'(\phi),$$

where $T'(\phi)$ is defined as $T(\phi)$ for the infinitary case except that the clause for first-order quantification becomes $T'(\exists \mathbf{x} \psi) = \exists \mathbf{x} \mathbf{x} \leq \mathbf{x}_{end} \wedge T'(\psi)$ (relativization). As in the proof of Theorem 14, we get the PSPACE upper bound for $\text{MC}(\text{FO})^*$. In the case \mathcal{A} has no infinite run, then the length K of the maximal finite run is in $\mathcal{O}(|Q|^3)$ and it can therefore be computed in polynomial-time. The prefixes s and t are defined as follows with $\Sigma = \{0, \dots, K-1, \perp\}$: $s = \{0\} \cdot \{1\} \cdots \{K-1\}$ and $t = \{\perp\}$. The map $T(\cdot)$ is defined as previously except that $T(\mathbf{x} \sim \mathbf{y}) = \bigvee_{\langle I, J \rangle \in P_{\sim}^4} I(\mathbf{x}) \wedge J(\mathbf{y})$ with $P_{\sim}^4 = \{\langle i, j \rangle \in \{0, \dots, K-1\}^2 \mid n_i = n_j\}$. The pure formula ϕ is translated into $\exists \mathbf{x}_{end} (\bigvee_{I \in P'_F} I(\mathbf{x}_{end})) \wedge \neg \perp(\mathbf{x}_{end}) \wedge T'(\phi)$, with $P'_F = \{i \in \{0, \dots, K-1\} \mid q_i \in F\}$. The formula $T'(\phi)$ is defined as $T(\phi)$ for the infinitary case except that the clause for first-order quantification becomes $T'(\exists \mathbf{x} \psi) = \exists \mathbf{x} \mathbf{x} \leq \mathbf{x}_{end} \wedge T'(\psi)$. \square

This improves the complexity bounds from [30]. Using the translation from LTL^\downarrow into $\text{FO}(\sim, <, +1)$ from Lemma 2, we deduce the following corollary.

Corollary 16. $\text{MC}(\text{LTL})^*$ and $\text{MC}(\text{LTL})^\omega$ are PSPACE-complete.

4. Model checking nondeterministic one-counter automata

In this section, we show that several model-checking problems over nondeterministic one-counter automata are undecidable by reducing decision problems for Minsky machines by following a principle introduced in [11]. Undecidability is preserved even in presence of a unique register. This is quite surprising since $\ast\text{-SAT-LTL}^\downarrow$ restricted to one register and satisfiability for $\text{FO}_2(\sim, <, +1)$ are decidable [7, 8].

In order to illustrate the significance of the following results, it is worth recalling that the halting problem for Minsky machines with incrementing errors is reducible to finitary satisfiability for LTL with one register [8]. We show below that, if we have existential model checking of one-counter automata instead of satisfiability, then we can use one-counter automata to refine the reduction in [8] so that runs with incrementing errors are excluded. More precisely, in the reduction in [8], we were not able to exclude incrementing errors because the logic is too weak to express that, for every decrement, the datum labelling it was seen before (remember that we have no past operators). Now, the one-counter automata are used to ensure that such faulty decrements cannot occur.

Theorem 17. $\text{MC}(\text{LTL})_1^*$ restricted to formulae using only the temporal operators \mathbf{X} and \mathbf{F} is Σ_1^0 -complete.

Proof. The Σ_1^0 upper bound is by an easy verification since the existence of a finite run (encoded in \mathbb{N}) verifying an $\text{LTL}_1^{\downarrow, Q}$ formula (encoded in first-order arithmetic) can be encoded by a Σ_1^0 formula. So, let us reduce the halting problem for two-counter automata to $\text{MC}(\text{LTL})_1^*$ restricted to $\{\mathbf{X}, \mathbf{F}\}$. Let $\mathcal{A} = \langle Q, q_I, \delta, F \rangle$ be a two-counter automaton: the set of instructions L is $\{\text{inc}, \text{dec}, \text{ifzero}\} \times \{1, 2\}$. Without any loss of generality, we can assume that all the instructions from q_I are incrementations. We build a one-counter automaton $\mathcal{B} = \langle Q', q'_I, \delta', F' \rangle$ and a sentence ϕ in $\text{LTL}_1^{\downarrow, Q'}$ such that \mathcal{A} reaches an accepting state iff $\mathcal{B} \models^* \phi$.

For each run in \mathcal{A} of the form

$$\begin{pmatrix} q_I \\ c_1^0 = 0 \\ c_2^0 = 0 \end{pmatrix} \xrightarrow{\text{inst}^0} \begin{pmatrix} q^1 \\ c_1^1 \\ c_2^1 \end{pmatrix} \xrightarrow{\text{inst}^1} \dots \begin{pmatrix} q^N \\ c_1^N \\ c_2^N \end{pmatrix}$$

where the inst^i 's are instructions, we associate a run in \mathcal{B} of the form below:

$$\begin{pmatrix} q_I \\ 0 \end{pmatrix} \xrightarrow{*} \begin{pmatrix} \langle q_I, \text{inst}^0, q^1 \rangle \\ n^1 \end{pmatrix} \xrightarrow{*} \begin{pmatrix} \langle q^1, \text{inst}^1, q^2 \rangle \\ n^2 \end{pmatrix} \dots \begin{pmatrix} \langle q^{N-1}, \text{inst}^{N-1}, q^N \rangle \\ n^N \end{pmatrix}$$

where $\xrightarrow{*}$ hides steps for updating the counter according to the constraints described below. The set of states Q' will contain the set of transitions δ from \mathcal{A} .

We first define the one-counter automaton $\mathcal{B} = \langle Q', q'_I, \delta', F' \rangle$. In order to ease the presentation, the construction of \mathcal{B} is mainly provided graphically.

- Q' is the following set of states:

$$\begin{aligned} Q' = & \delta \uplus \{q_I\} \uplus \{i_0\} \\ & \uplus \{i_t^{last}, i_t^{-last} \mid t = \langle q, \text{inc}, c, q' \rangle \in \delta\} \\ & \uplus \{d_t^{last}, d_t^{-last} \mid t = \langle q, \text{dec}, c, q' \rangle \in \delta\} \\ & \uplus \{z_t^{down} \mid t = \langle q, \text{ifzero}, c, q' \rangle \in \delta\} \\ & \uplus \{z_q \mid q \in Q\} \uplus Q_{aux} \end{aligned}$$

where Q_{aux} is a set of auxiliary states that we do not specify (but which can be identified as the states with no label in Figures 4, 5 and 6),

- F' is the set of states $\{z_q \mid q \in F\}$.
- The transition relation δ' is the smallest transition relation satisfying the conditions below:
 - The transitions in Figure 3 belong to δ' .
 - For each incrementation transition $t = \langle q_I, \text{inc}, c, q \rangle$, the transitions in Figure 4 belong to δ' .
 - For each decrementation transition $t = \langle q_I, \text{dec}, c, q \rangle$, the transitions in Figure 5 belong to δ' .
 - For each zero-test transition $t = \langle q_I, \text{ifzero}, c, q \rangle$, the transitions in Figure 6 belong to δ' .

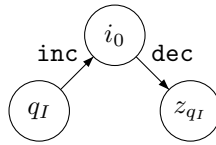


Figure 3: Initial transitions in δ'

In runs of \mathcal{B} , we are only interested in configurations whose state belongs to δ . The structure of \mathcal{B} ensures that the sequence of transitions in \mathcal{A} is valid assuming that we ignore the intermediate (auxiliary or busy) configurations

Before defining the formula ϕ , let us introduce a few intermediate formulae that allow us to check whether the current configuration has a state belonging to a specific set. For each counter $i \in \{1, 2\}$, we define the formulae below:

- I_i is the disjunction of i_0 with all the transitions t that increment the counter i in \mathcal{A} , hence $I_i = i_0 \vee \bigvee_{\{t \in \delta \mid t = \langle q, \text{inc}, i, q' \rangle\}} t$.

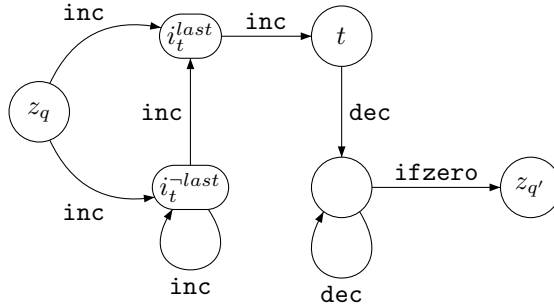


Figure 4: Gadget in \mathcal{B} for encoding an incrementation from \mathcal{A}

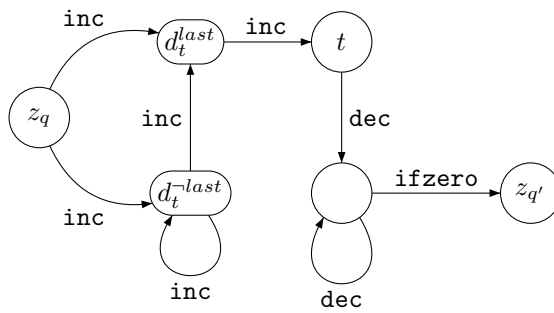


Figure 5: Gadget in \mathcal{B} for encoding a decrementation from \mathcal{A}

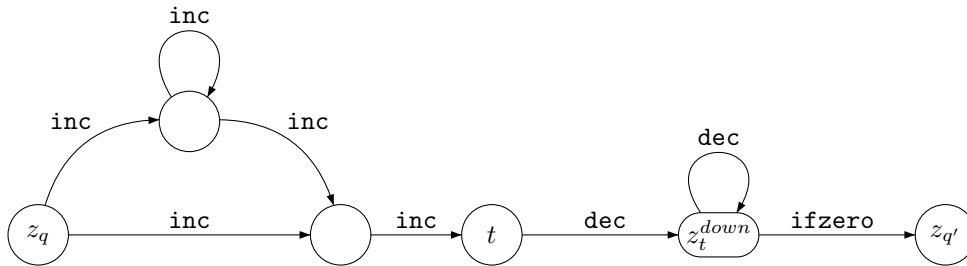


Figure 6: Gadget in \mathcal{B} for encoding a zero-test from \mathcal{A}

- D_i is the disjunction of i_0 with all the transitions t that decrement the counter i in \mathcal{A} , hence $D_i = i_0 \vee \bigvee_{\{t \in \delta | t = \langle q, \text{dec}, i, q' \rangle\}} t$.
- I_i^{last} is the disjunction of all states of the form i_t^{last} where t is a transition that increments the counter i , hence $I_i^{last} = \bigvee_{\{t \in \delta | t = \langle q, \text{inc}, i, q' \rangle\}} i_t^{last}$.
- I_i^{-last} is the disjunction of all states of the form i_t^{-last} where t is a transition that increments the counter i , hence $I_i^{-last} = \bigvee_{\{t \in \delta | t = \langle q, \text{inc}, i, q' \rangle\}} i_t^{-last}$.
- D_i^{last} is the disjunction of all states of the form d_t^{last} where t is a transition that decrements the counter i , hence $D_i^{last} = \bigvee_{\{t \in \delta | t = \langle q, \text{dec}, i, q' \rangle\}} d_t^{last}$.
- D_i^{-last} is the disjunction of all states of the form d_t^{-last} where t is a transition that decrements the counter i , hence $D_i^{-last} = \bigvee_{\{t \in \delta | t = \langle q, \text{dec}, i, q' \rangle\}} d_t^{-last}$.
- Z_i is the disjunction of all the transitions t that test to zero the counter i in \mathcal{A} , hence $Z_i = \bigvee_{\{t \in \delta | t = \langle q, \text{ifzero}, i, q' \rangle\}} t$.
- Z_i^{down} is the disjunction of the states of the form z_t^{down} where t is a zero-test on the counter i , hence $Z_i^{down} = \bigvee_{\{t \in \delta | t = \langle q, \text{ifzero}, i, q' \rangle\}} z_t^{down}$.

In order to define ϕ , we take advantage of the structure of \mathcal{B} so that to match runs of \mathcal{B} with runs of \mathcal{A} . A crucial idea consists in associating to each action on one of the two counters, a natural number so that an incrementation gets a new value. Moreover, we require that the natural number associated to an incrementation is obtained by increasing by one the natural number associated to the previous incrementation. We satisfy a similar property for the natural numbers associated to decrements except that these values should not exceed the value associated to the previous incrementation. In this way, we guarantee that there are no more decrements than incrementations. In order to simulate the zero-test, we reach a value above all the values that have been used so far. Then we check that for all the smaller values that are associated to an incrementation, it is also associated to a decrementation (for the same counter).

In the following formulae, we use \mathbf{G}^+ and \mathbf{F}^+ to represent the formulae XG and XF, respectively. We also omit the subscript “1” in \downarrow_1 and \uparrow_1 because we assume that we always use the same register. For each counter $i \in \{1, 2\}$, we define the following formulae:

- (i) After each configuration satisfying I_i , there is no strict future configuration satisfying I_i with the same data value:

$$\mathbf{G}(I_i \Rightarrow \downarrow \mathbf{G}^+(I_i \Rightarrow \neg \uparrow))$$

- (ii) After each configuration satisfying D_i , there is no strict future configuration satisfying D_i with the same data value:

$$\mathbf{G}(D_i \Rightarrow \downarrow \mathbf{G}^+(D_i \Rightarrow \neg \uparrow))$$

- (iii) After each configuration satisfying D_i , there is no strict future configuration satisfying I_i with the same data value:

$$\mathbf{G}(D_i \Rightarrow \downarrow \mathbf{G}^+(I_i \Rightarrow \neg \uparrow))$$

- (iv) When a new data value is needed for an incrementation of the counter i , the chosen value is exactly the next value after the greatest value used so far for an incrementation of the counter i :

$$\begin{aligned} & \mathbf{G}(I_i \Rightarrow (\downarrow \mathbf{F}(I_i^{-last} \wedge \uparrow) \Rightarrow \downarrow \mathbf{F}(I_i^{last} \wedge \uparrow))) \\ & \wedge \mathbf{G}((I_i^{last} \vee I_i^{-last}) \Rightarrow \downarrow \mathbf{G}^+(I_i \Rightarrow \neg \uparrow)) \end{aligned}$$

- (v) When a new data value is needed for a decrementation of the counter i , the chosen value is exactly the next value after the greatest value used so far for a decrementation of the counter i :

$$\begin{aligned} & \mathbf{G}(D_i \Rightarrow (\downarrow \mathbf{F}(D_i^{-last} \wedge \uparrow) \Rightarrow \downarrow \mathbf{F}(D_i^{last} \wedge \uparrow))) \\ & \wedge \mathbf{G}((D_i^{last} \vee D_i^{-last}) \Rightarrow \downarrow \mathbf{G}^+(D_i \Rightarrow \neg \uparrow)) \end{aligned}$$

- (vi) The data value associated to a decrementation of the counter i is never strictly greater than the greatest previous value used in incrementations of the counter i :

$$\begin{aligned} & \mathbf{G}(I_i \Rightarrow (\downarrow \mathbf{F}(D_i^{-last} \wedge \uparrow) \Rightarrow \downarrow \mathbf{F}(I_i^{last} \wedge \uparrow))) \\ & \wedge \mathbf{G}(I_i \Rightarrow (\downarrow \mathbf{F}(D_i^{last} \wedge \uparrow) \Rightarrow \downarrow \mathbf{F}(I_i^{last} \wedge \uparrow))) \\ & \wedge \mathbf{G}(D_i^{-last} \Rightarrow \downarrow \mathbf{G}^+(I_i^{last} \Rightarrow \neg \uparrow)) \end{aligned}$$

- (vii) For each configuration satisfying Z_i , the associated data value is always strictly greater than the greatest previous value used in incrementations of the counter i :

$$\mathbf{G}(I_i \Rightarrow \downarrow \mathbf{G}(Z_i \Rightarrow \neg \uparrow))$$

- (viii) When the automaton \mathcal{B} is in the decrementing slope to encode a zero-test in \mathcal{A} , which means when the formula Z_i^{down} is satisfied, and when a data value already used for an incrementation is met, then the same data value is used previously for a decrementation in \mathcal{B} :

$$\neg \mathbf{F}(I_i \wedge \downarrow \mathbf{F}(Z_i^{down} \wedge \uparrow) \wedge \neg \downarrow \mathbf{F}(\uparrow \wedge D_i)) \wedge \neg \mathbf{F}(Z_i^{down} \wedge \downarrow \mathbf{F}(D_i \wedge \uparrow))$$

Let us recall the book-keeping of the values.

- A new value used for an incrementation is always one plus the greatest value used so far for an incrementation (see (iv)). The first counter value for an incrementation is 2.
- A new value used for decrementation is always 1 + the greatest value used so far for a decrementation (see (v)), and is always smaller or equal to the greatest value used so far for an incrementation (see (vi)). The first counter value for a decrementation is 2.
- Zero-tests consist in:
 - (1) going to a value strictly greater than any value used so far for incrementations (encoded in \mathcal{B} and see (vii)),
 - (2) then decrementing the counter to zero (encoded in \mathcal{B}) and whenever a value is met that is used for an incrementation, check that a corresponding decrementation has occurred before (see (viii)).

In order to ease the comprehension, we explain why the rule (vi) ensures that the value associated to a decrementation of the counter i is never strictly greater than the value used for the last incrementation of the same counter i . First, we assume that the rules (i)–(vi) are satisfied and *ad absurdum* we suppose that the value used for a decrementation is strictly greater than the value used for the last incrementation of the counter i . If this value is greater of exactly one unit, then we are in the case of the second line of the formula given by the rule (vi). Hence, there must exist an incrementation with the same value as the one for the decrementation, and this incrementation necessarily happens between the first considered incrementation and the decrementation, according to the rules (i)–(iii). This leads to a contradiction because the first considered incrementation is not the last one. Secondly, suppose that the value associated to the decrementation is greater of k units with $k > 1$. We are in the case of the first line of the formula given by the rule (vi), and consequently there exists an incrementation after the first considered incrementation which has an associated value greater of one unit. The last line of the formula of the rule (vi) ensures that this incrementation occurs necessarily before the decrementation, which leads again to a contradiction, because the first considered incrementation cannot be the last one.

Figure 7 gives an example of the beginning of a run of \mathcal{B} which respects the rules (i)–(viii) and that encodes the following sequence of instructions (**inc**, 1), (**inc**, 1), (**dec**, 1), (**dec**, 1), (**ifzero**, 1). In the decreasing part after the position labeled by Z_1 , each value used in a previous incrementation can be matched with a value associated to a decrementation. The formula ϕ is defined as the conjunction of (i)–(viii) plus (ix) that specifies that a state in F' is reached. Now consider any run of \mathcal{B} which satisfies (i)–(viii). For any counter $c \in \{1, 2\}$, we can define its value as the number of I_t letters with t of the form $\langle q, \text{inc}, c, q' \rangle$ for which a

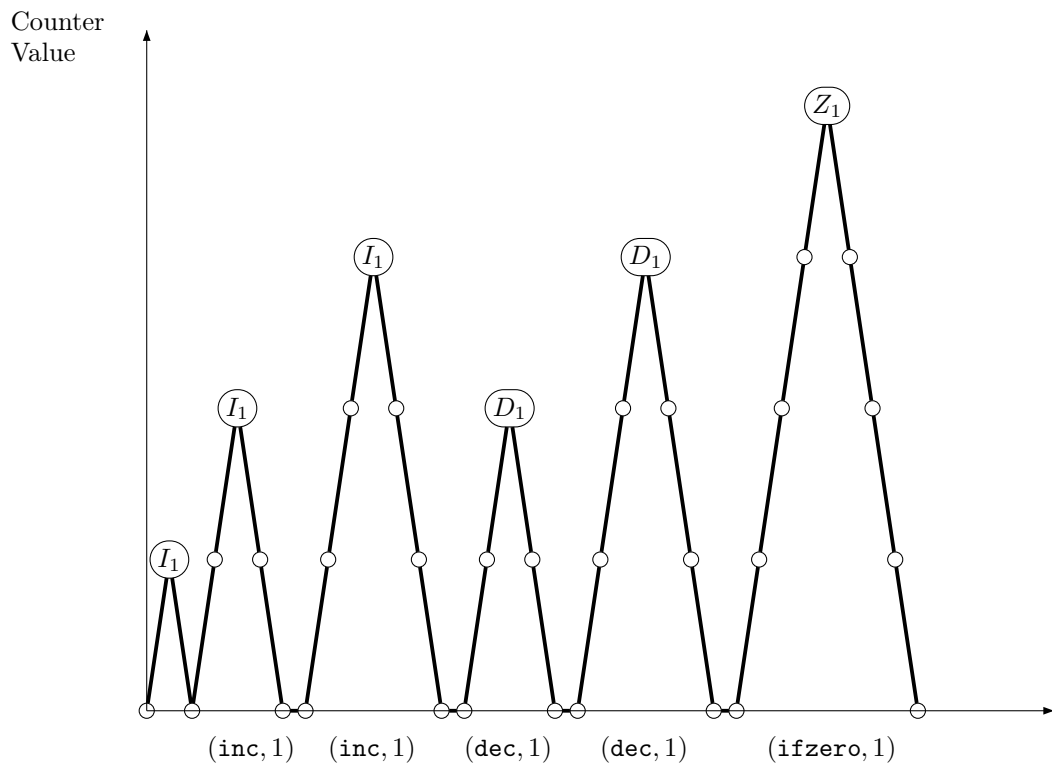


Figure 7: Run for \mathcal{B} satisfying the rules (i)–(viii)

later letter $\langle q_1, \text{dec}, c, q'_1 \rangle$ with the same value of the counter \mathcal{B} has not yet occurred. We will now prove that $\mathcal{B} \models^* \phi$ if and only if the automaton \mathcal{A} has an accepting run.

Let $\rho = \langle p_0, 0 \rangle \xrightarrow{a_0} \langle p_1, n_1 \rangle \xrightarrow{a_1} \langle p_2, n_2 \rangle \dots \langle p_m, n_m \rangle$ be a finite run of \mathcal{B} satisfying the rules (i)–(viii) and such that $p_0 = q_I$ and $p_m = q$ for some $q \in Q$. We consider the sequence of indices $i_1, \dots, i_k \in \{0, \dots, m\}$ such that for all $j \in \{1, \dots, m\}$, $p_{i_j} \in \delta$ and such that there is no $i \in \{1, \dots, m\}$ with $p_i \in \delta$ and $i \notin \{i_1, \dots, i_k\}$. We will show that the sequence $p_{i_1} p_{i_2} \dots p_{i_k}$ induces a run of \mathcal{A} . This means that there exist k configurations $c_1, c_2, \dots, c_k \in Q \times \mathbb{N}^2$ such that $\langle q_I, 0, 0 \rangle \xrightarrow{p_{i_1}} c_1 \xrightarrow{p_{i_2}} c_2 \dots \xrightarrow{p_{i_k}} c_k$ is a run of \mathcal{A} .

The proof is by induction on k . If $k = 1$, then by construction of the automaton \mathcal{B} , there exist $i \in \{1, 2\}$ and $q' \in Q$ such that $p_{i_1} = \langle q_0, \text{inc}, i, q' \rangle$. This is simply due to the fact that we have assumed that any instruction starting in q_I is an incrementation. Since it is always possible to perform an incrementation, there is a configuration $c_1 \in Q \times \mathbb{N}^2$ such that $\langle q_I, 0, 0 \rangle \xrightarrow{p_{i_1}} c_1$.

We suppose that the property is true for k and we show that it also holds for $k + 1$.

First, let us write down the properties verified by the sequence

$$\langle p_{i_0}, n_{i_0} \rangle, \dots, \langle p_{i_k}, n_{i_k} \rangle$$

made of configurations of \mathcal{B} . For each counter $i \in \{1, 2\}$, we write Inc_i to denote the set $\{j \in \{1, \dots, k\} \mid p_{i_j} \text{ is of the form } \langle q, \text{inc}, i, q' \rangle\}$ and Dec_i to denote the set $\{j \in \{1, \dots, k\} \mid p_{i_j} \text{ is of the form } \langle q, \text{dec}, i, q' \rangle\}$. Let i be one of the counters in $\{1, 2\}$. The rule (i) ensures that for every $j \in Inc_i$, $n_{i_j} > 1$, and for all $j, \ell \in Inc_i$, $n_{i_j} \neq n_{i_\ell}$. This is because i_0 is a disjunct of I_i , the counter value in the state i_0 is always 1 and for all $j \in Inc_i$, p_{i_j} satisfies I_i . Furthermore the rule (iv) implies that for all $j, \ell \in Inc_i$ such that $j < \ell$, if there is no $j' \in Inc_i$ such that $j < j' < \ell$, then necessarily $n_{i_\ell} = n_{i_j} + 1$. Moreover, if j is the smallest index of Inc_i then $n_{i_j} = 2$. In fact, if j is the smallest index of Inc_i , then n_{i_j} is greater or equal to 2 (because the integer value in i_0 is always 1). If n_{i_j} is strictly greater than 2, then the run of \mathcal{B} should reach a state that satisfies I_i^{last} or $I_i^{-\text{last}}$ with a value equal to 2, but since j is the smallest index of Inc_i , the rule (iv) would not be satisfied. To show the other property about the indices in Inc_i , this can be done by induction on the indices of Inc_i by using again the rule (iv). Similarly, it can be proved that the set Dec_i verifies the same properties. Hence, $\{n_{i_j} \mid j \in Inc_i\} = \{2, \dots, |Inc_i| + 1\}$ and $\{n_{i_j} \mid j \in Dec_i\} = \{2, \dots, |Dec_i| + 1\}$. Finally, the rule (vi) guarantees that for every $j \in Dec_i$, there is $\ell \in Inc_i$ such that $i_\ell \leq i_j$ and $n_{i_j} \leq n_{i_\ell}$. By combining these different properties, we deduce that $|Dec_i| \leq |Inc_i|$.

We suppose that $p_{i_k} = \langle q, a', i', q' \rangle$. By construction of \mathcal{B} , we have $p_{i_{k+1}} = \langle q', a, i, q'' \rangle$. If a is equal to **inc**, then the property is satisfied because an incrementation can always be performed (unlike decrements and zero-tests). Now, suppose that $a = \text{dec}$. The transition $p_{i_{k+1}} = \langle q', a, i, q'' \rangle$ is not firable only if $|Dec_i| = |Inc_i|$ (the number of incrementations is equal to the number of decrements). This situation cannot occur since ρ satisfies the rules (i)–(viii), and therefore $n_{i_{k+1}} = n_{i_H} + 1$ where H is the greatest index of $|Dec_i|$ and there exists $h \in |Inc_i|$ such that $i_h \leq i_{k+1}$ and $n_{i_{k+1}} \leq n_{i_h}$. Hence, if $|Dec_i| = |Inc_i|$, according to the previous properties, we would have that there exists $j \in Dec_i$ such that $n_{i_h} = n_{i_j}$ and consequently $n_{i_H} + 1 \leq n_{i_j}$ which leads to a contradiction (by definition of H). Now, suppose that $a = \text{ifzero}$. The transition $p_{i_{k+1}}$ is not firable only if $|Inc_i| > |Dec_i|$ (there are more incrementations than decrements). This situation cannot occur since ρ satisfies the rules (i)–(viii) and according to the rule (vii) and to the properties verified by Inc_i , for all $j \in Inc_i$, $n_{i_j} < n_{i_{k+1}}$. After the i_{k+1} th configuration, the $n_{i_{k+1}}$ next configurations contain a state that satisfies Z_i^{down} . If $|Inc_i| > |Dec_i|$, then this means that there is an index $h \in Inc_i$ such that for all $j \in Dec_i$, $n_{i_j} < n_{i_h}$ and there exists also $l \in \{i_{k+1}, \dots, i_{k+1} + n_{i_{k+1}}\}$ such that p_l satisfies Z_i^{down} and $n_l = n_h$, which is in contradiction with the rule (viii).

We conclude that if ρ is a finite run of \mathcal{B} satisfying the rules (i)–(viii) and visiting a state z_q in F' then there is a corresponding run in the two-counter automaton \mathcal{A} starting from the initial configuration $\langle q_I, 0, 0 \rangle$ and visiting the accepting state q .

Now, we consider a run of \mathcal{A} of the form $\langle q_I, 0, 0 \rangle \xrightarrow{t_0} c_1 \xrightarrow{t_1} \dots \xrightarrow{t_{h-1}} c_h$. We show how to build a run of the one-counter automaton \mathcal{B} , $\langle p_0, 0 \rangle \rightarrow \langle p_1, n_1 \rangle \rightarrow \dots \rightarrow \langle p_m, n_m \rangle$ with $p_0 = q_I$ and $p_m = z_q$ for some $q \in Q$. We introduce similar notations as in the converse case. For such a run, we consider the sequence of indices $i_1, \dots, i_k \in \{0, \dots, m\}$ such that for all $j \in \{1, \dots, m\}$, $p_{i_j} \in \delta$ and such that there is no $i \in \{1, \dots, m\}$ verifying $p_i \in \delta$ and $i \notin \{i_1, \dots, i_k\}$. For each counter $i \in \{1, 2\}$, we write Inc_i to denote the set $\{j \in \{0, \dots, k\} \mid$

p_{i_j} is of the form $\langle q, \text{inc}, i, q' \rangle$ and Dec_i to denote the set $\{j \in \{0, \dots, k\} \mid p_{i_j} \text{ is of the form } \langle q, \text{dec}, i, q' \rangle\}$. Finally, we define the set $Zero_i = \{j \in \{0, \dots, k\} \mid p_{i_j} \text{ is of the form } \langle q, \text{ifzero}, i, q' \rangle\}$. We build a run ρ of \mathcal{B} such that the following properties are verified :

- (a) $k = h$ and for all $j \in \{1, \dots, k\}$, $p_{i_j} = t_{j-1}$,
- (b) if j is the smallest index of Inc_i , then $n_{i_j} = 2$,
- (c) if j is the smallest index of Dec_i , then $n_{i_j} = 2$,
- (d) for all $j, \ell \in Inc_i$ such that $j < \ell$, if there is no $j' \in Inc_i$ such that $j < j' < \ell$, then $n_{i_\ell} = n_{i_j} + 1$,
- (e) for all $j, \ell \in Dec_i$ such that $j < \ell$, if there is no $j' \in Inc_i$ such that $j < j' < \ell$, then $n_{i_\ell} = n_{i_j} + 1$,
- (f) for all $j \in Dec_i$, there exists $\ell \in Inc_i$ such that $i_\ell < i_j$ and $n_{i_j} \leq n_{i_\ell}$,
- (g) for all $j \in Zero_i$, and for all $\ell \in Inc_i$ such that $i_\ell < i_j$, we have $n_{i_\ell} < n_{i_j}$ and there is $m \in Inc_i$ such that $i_m < i_j$ and $n_{i_j} = n_{i_m} + 1$.

By construction of \mathcal{B} , it is possible to build a run ρ of \mathcal{B} that satisfies the properties (a)–(g).

Now, we suppose that ρ is a run of \mathcal{B} verifying these properties and it remains to check that ρ satisfies the rules (i)–(viii). First, we consider the rules (i)–(ii). These two rules are satisfied because all the elements of Inc_i and of Dec_i are built with distinct values for incrementations and decrementsations. The rule (iii) is satisfied because of the properties (e) and (f). The rule (iv) is satisfied, because if the run is in a position i_j with $j \in Inc_i$ and if there exists a position ℓ in the future which satisfies I_i^{-last} , then there exists a position $i_{j'}$ such that $\ell < i_{j'}$ with $j' \in Inc_i$ and $n_{i_{j'}} > n_{i_j} + 1$ (by construction of \mathcal{B} and by (d)). Moreover, the definition of \mathcal{B} implies there exists a position h such that $i_j < h < \ell$, h satisfies I_i^{last} , $n_h = n_{i_j}$, q_{h+1} satisfies I_i and $n_{h+1} = n_{i_j} + 1$. Similar arguments are used to establish that the rule (v) is satisfied by using (c) and (e). The rule (vi) is satisfied because of the property (f). Finally the rules (vii)–(viii) are satisfied by using (g) and the properties about the sets Inc_i and Dec_i . Hence if there is a run of \mathcal{A} leaving from $\langle q_I, 0, 0 \rangle$ and visiting a state q in F , we can build a finite run ρ of \mathcal{B} such that $\rho \models \phi$.

Furthermore the formula ϕ uses only the temporal operators \mathbf{X} and \mathbf{F} (the operator \mathbf{G} can be easily obtained from \mathbf{F}). \square

Theorem 18. $\text{MC}(\text{LTL})_1^\omega$ restricted to $\{\mathbf{X}, \mathbf{F}\}$ is Σ_1^1 -complete.

The proof is similar to the proof of Theorem 17 except that instead of reducing the halting problem for Minsky machines, we reduce the recurrence problem for nondeterministic Minsky machines that is known to be Σ_1^1 -hard [20]. The Σ_1^1 upper bound is by an easy verification since an accepting run can be viewed as a function $f : \mathbb{N} \rightarrow \mathbb{N}$ and then checking that it satisfies an $\text{LTL}_1^{\downarrow, Q}$ formula can be expressed in first-order arithmetic. Another consequence of the Purification Lemma is the result below.

Theorem 19. $\text{PureMC}(\text{LTL})_1^*$ restricted to $\{\mathbf{X}, \mathbf{F}\}$ is Σ_1^0 -complete. $\text{PureMC}(\text{LTL})_1^\omega$ restricted to $\{\mathbf{X}, \mathbf{F}\}$ is Σ_1^1 -complete.

This refines results stated in [30].

Using Theorem 3.2(a) in [8], we can obtain the following corollary by a direct analysis of the formulae involved in the proof of Theorem 17 (every temporal operator is prefixed by a freeze operator or can occur equivalently in such a form).

Corollary 20. $\text{MC}(\text{FO})_2^*$ [resp. $\text{MC}(\text{FO})_2^\omega$] without the predicate $+1$ is Σ_1^0 -complete [resp. Σ_1^1 -complete] and $\text{PureMC}(\text{FO})_4^*$ [resp. $\text{PureMC}(\text{FO})_4^\omega$] is Σ_1^0 -complete [resp. Σ_1^1 -complete].

The absence of the predicate $+1$ in the above corollary is due to the fact that in the proof of Theorem 17, \mathbf{X} occurs only to encode \mathbf{F}^+ and \mathbf{G}^+ . The above-mentioned undecidability is true even if we restrict ourselves to one-counter automata for which there are no transitions with identical instructions leaving from the same state. A one-counter automaton \mathcal{A} is *weakly deterministic* whenever for every state q , if $\langle q, l, q' \rangle, \langle q, l', q'' \rangle \in \delta$, we have $l = l'$ implies $q' = q''$. The transition systems induced by these automata are not necessarily deterministic.

Theorem 21. $\text{PureMC}(\text{LTL})_1^*$ [resp. $\text{PureMC}(\text{LTL})_1^\omega$] restricted to weakly deterministic one-counter automata is Σ_1^0 -complete [resp. Σ_1^1 -complete].

Proof. In the proof of the Purification Lemma, weak determinism of the one-counter automata is preserved. It is sufficient to show that given a one-counter automaton \mathcal{A} and a sentence ϕ in $\text{LTL}^{\downarrow, Q}$, one can compute a weakly deterministic automaton \mathcal{A}' and ϕ' in $\text{LTL}^{\downarrow, Q'}$ ($Q \subseteq Q'$) such that $\mathcal{A} \models^* \phi$ [resp. $\mathcal{A} \models^\omega \phi$] iff $\mathcal{A}' \models^* \phi'$ [resp. $\mathcal{A}' \models^\omega \phi'$].

Figure 8 illustrates with examples how transitions from a state with identical instructions can be transformed so that to obtain a weakly deterministic automaton. In Figure 8, we have omitted the transitions labelled by a zero-test or a decrementation when they are never fired. This can be easily generalized to all the transitions of \mathcal{A} . The formula ϕ' is defined as $T(\phi)$ with the map T that is homomorphic for Boolean operators and \downarrow_r , and its restriction to atomic formulae is identity. It remains to define the map for the temporal operators, which corresponds to perform a relativization:

- $T(\phi_1 \cup \phi_2) = ((\bigvee_{q \in Q} q \Rightarrow T(\phi_1)) \cup (\bigvee_{q \in Q} q \wedge T(\phi_2)))$,
- $T(\mathbf{X}\psi) = \mathbf{X}((\neg \bigvee_{q \in Q} q) \cup (\bigvee_{q \in Q} q \wedge T(\psi)))$.

It can be easily proved that \mathcal{A}' and ϕ' satisfy the desired properties. □

5. Conclusion

In the paper, we have studied complexity issues related to the model-checking problem for LTL with registers over one-counter automata. Our results are quite different from those for satisfiability. We have shown that model checking LTL^\downarrow restricted to the operators $\{\mathbf{X}, \mathbf{F}\}$ and $\text{FO}_2(\sim, <, +1)$ over one-counter automata is undecidable, which contrasts with the decidability of many verification problems for one-counter automata [27, 28, 29] and with the results in [7, 8]. For instance, we have shown that model checking nondeterministic one-counter automata over LTL^\downarrow restricted to a unique register and without alphabet [resp. $\text{FO}_2(\sim, <, +1)$] is already Σ_1^1 -complete in the infinitary case. On the decidability side, the PSPACE upper bound for model checking LTL^\downarrow and $\text{FO}(\sim, <, +1)$ over deterministic one-counter automata in the infinitary and finitary cases is established by using in an essential way [26] (and simplifying the proofs from [30]). In particular, we have established that the runs of deterministic one-counter automata admit descriptions that require polynomial size only. Hence, our results essentially deal with LTL with registers but they can be also understood as a contribution to refine the decidability border for problems on one-counter automata.

Viewing runs as data words is an idea that can be pushed further. Indeed, our results pave the way for model checking memoryful (linear-time) logics (possibly extended to multicounters) over other classes of operational models that are known to admit powerful techniques for solving verification tasks. For instance, the reachability relation is known to be Presburger-definable for reversal-bounded counter automata [32]. Nevertheless, model checking LTL^\downarrow over this class of counter machines has been recently shown undecidable [33]; other subclasses of counter machines for which the reachability problem is decidable have been considered in this recent work.

Acknowledgement: We would like to thank Philippe Schnoebelen for suggesting simplifications in the proofs of Lemma 5 and Proposition 7 and Luc Segoufin for fruitful discussions that lead us to improve significantly the results from [30].

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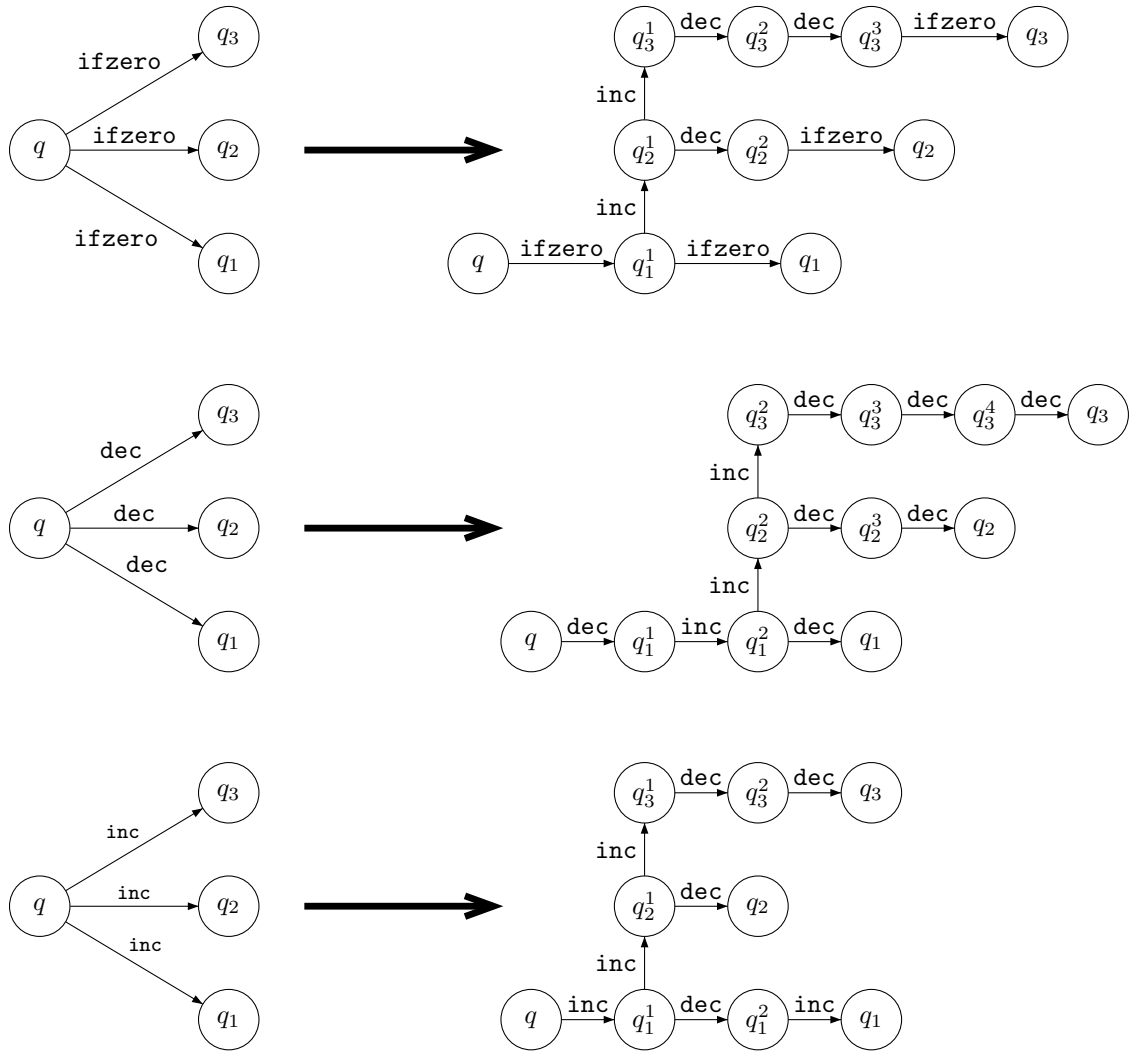


Figure 8: Weak determinization of one-counter automata

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